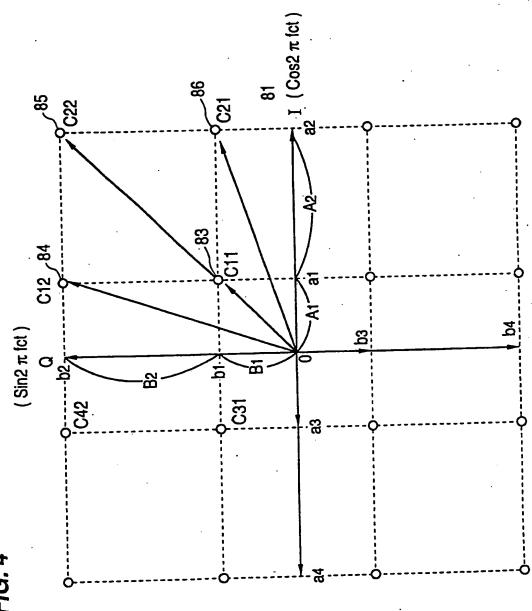


FIG. 3



:IG: 1



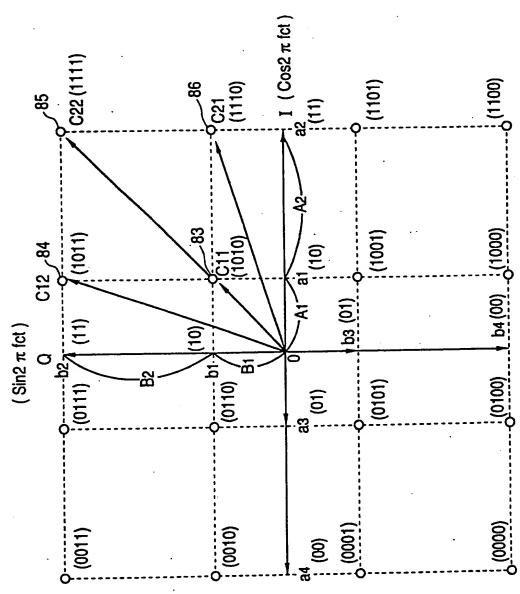


FIG. 5

FIG. 6

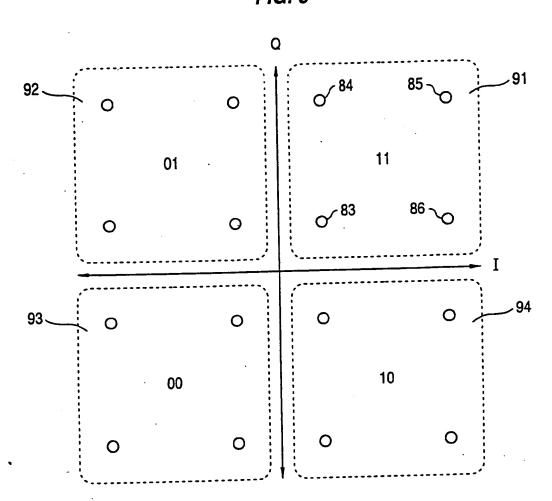


FIG. 7

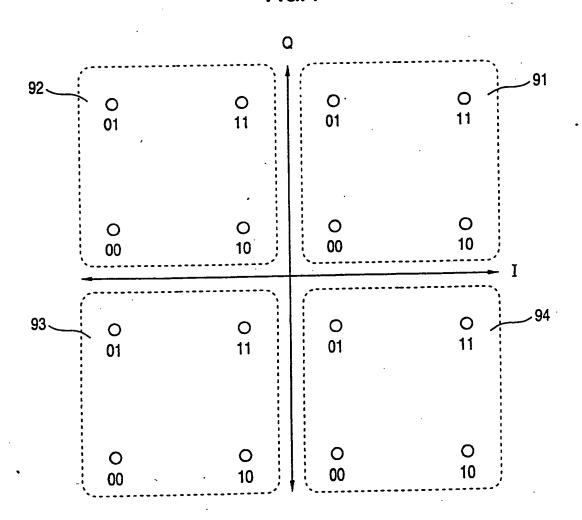
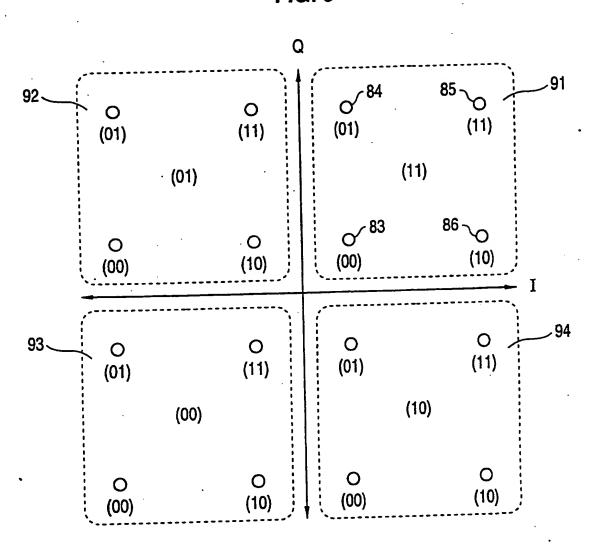
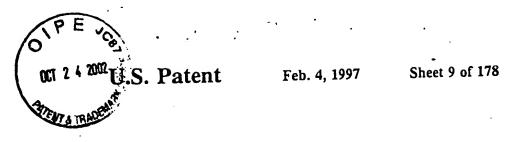


FIG. 8





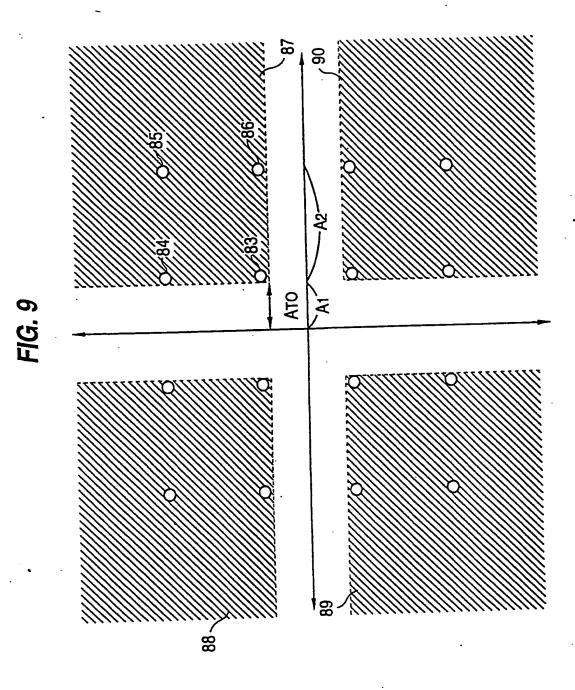
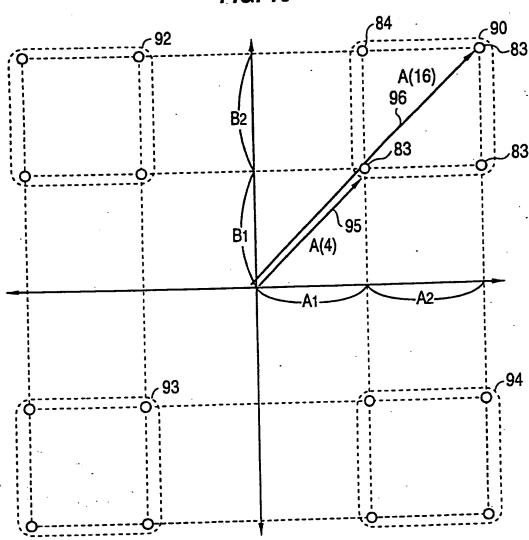


FIG. 10



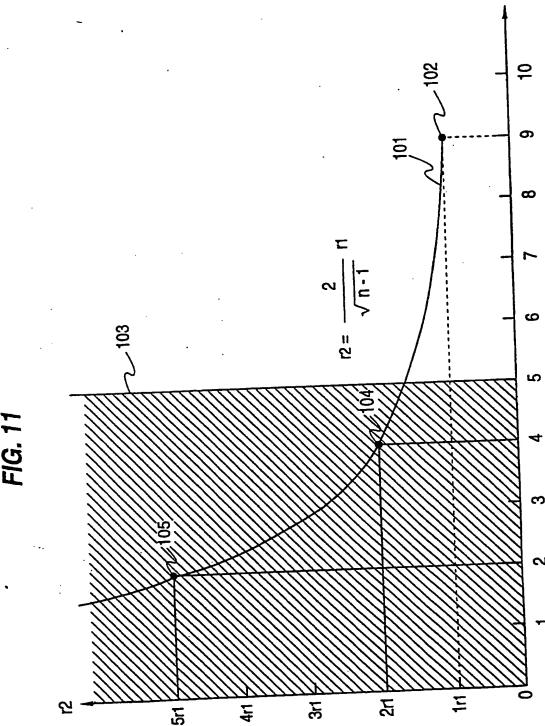
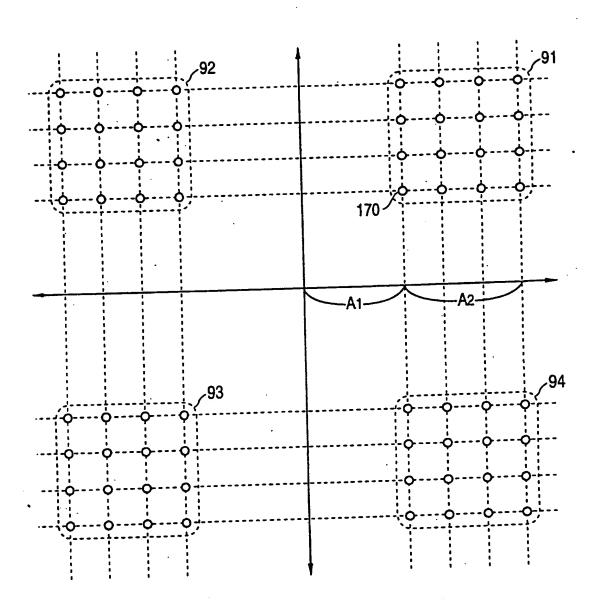
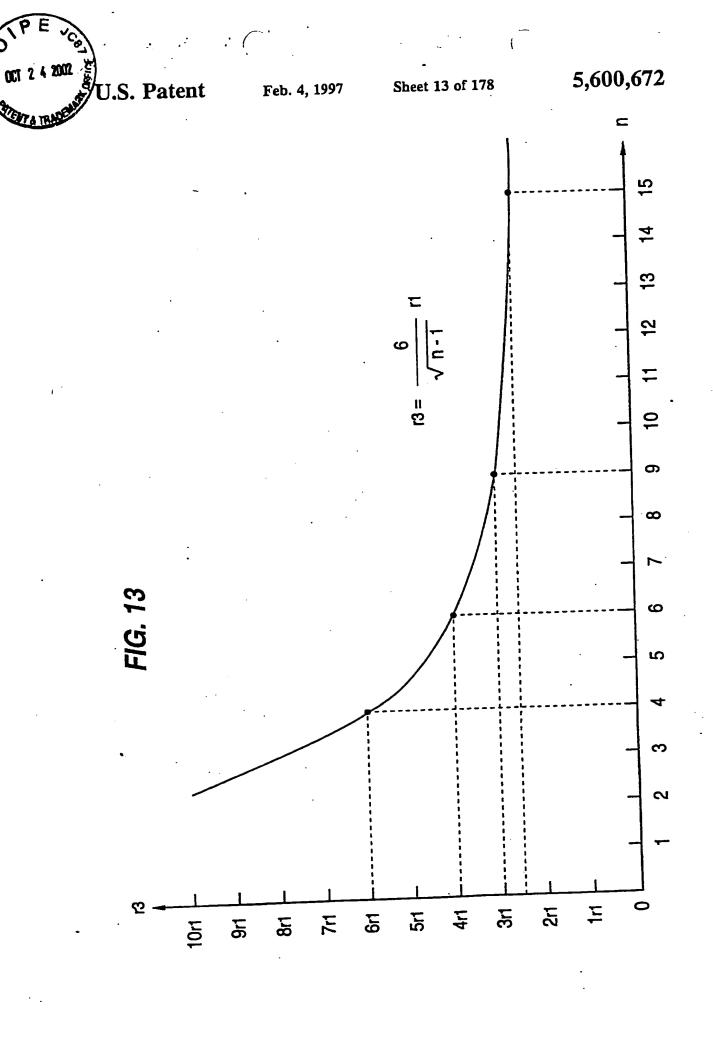


FIG. 12







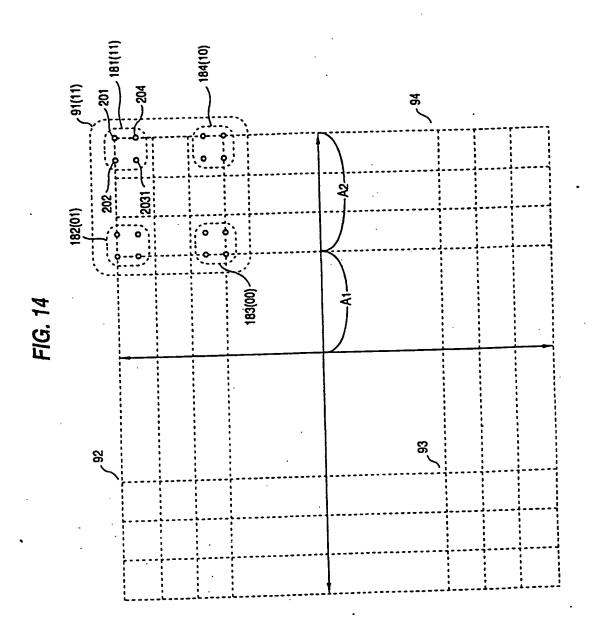
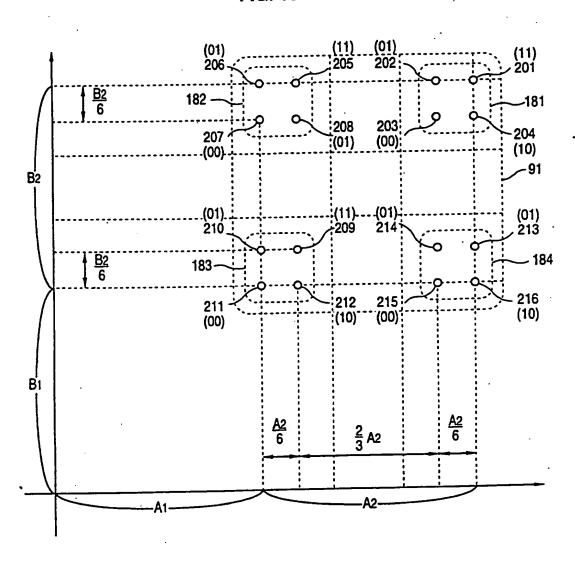
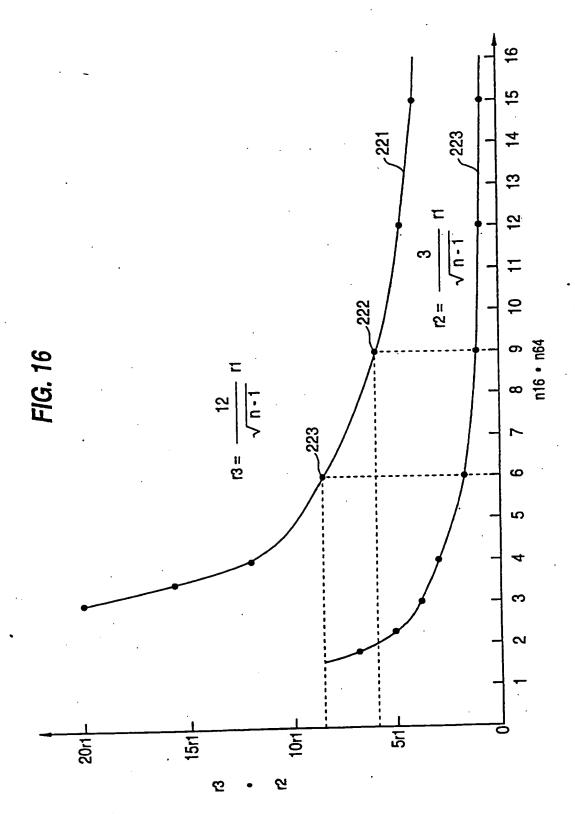


FIG. 15





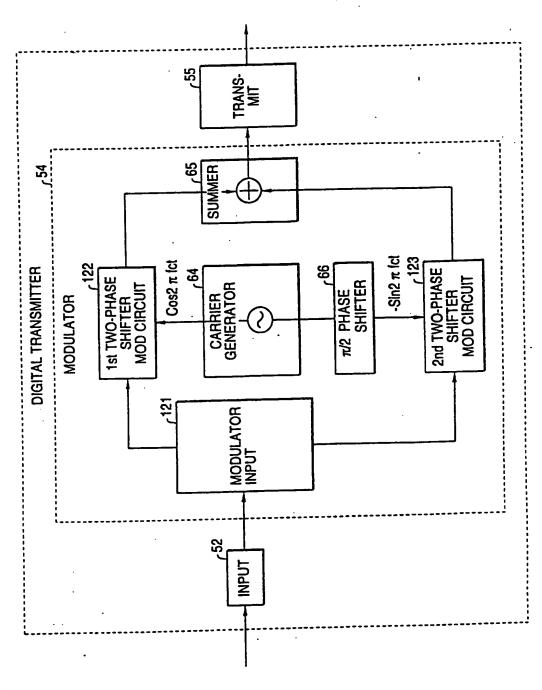
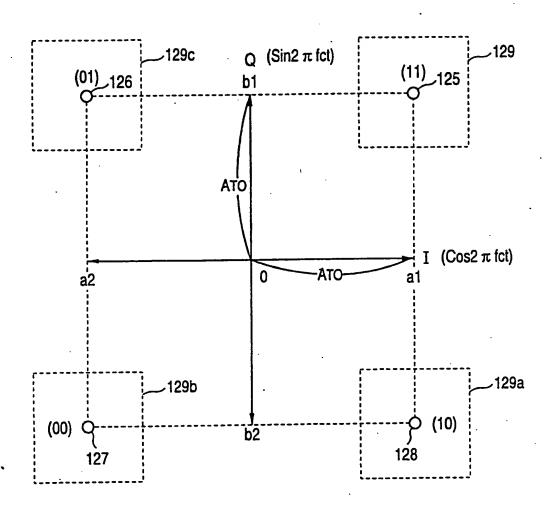
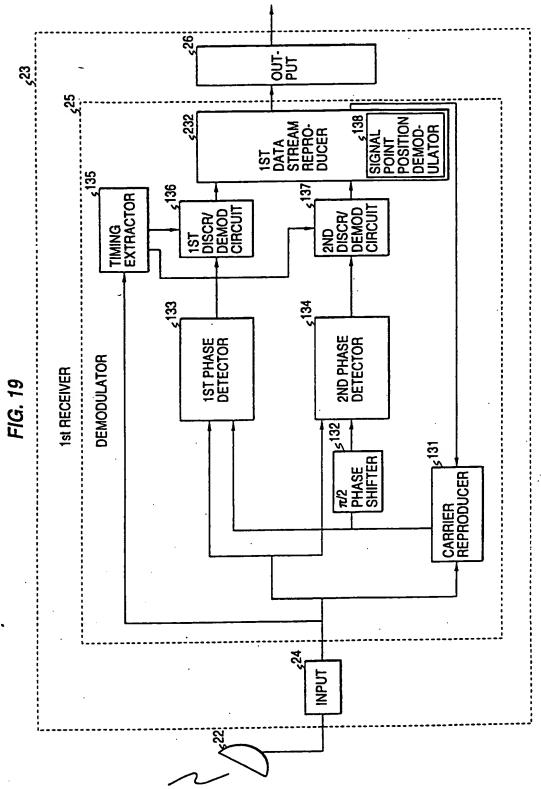
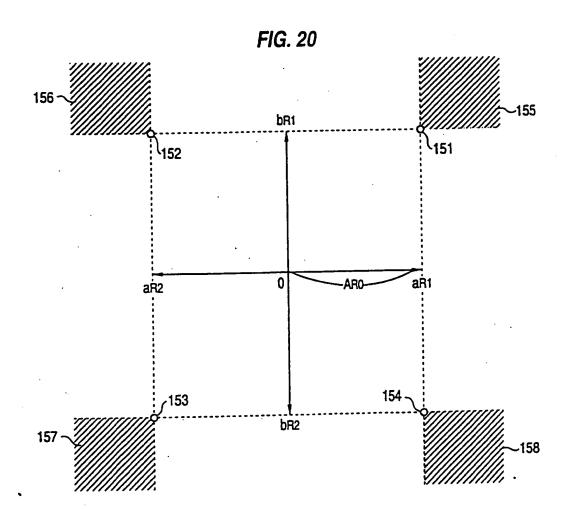


FIG. 17

FIG. 18







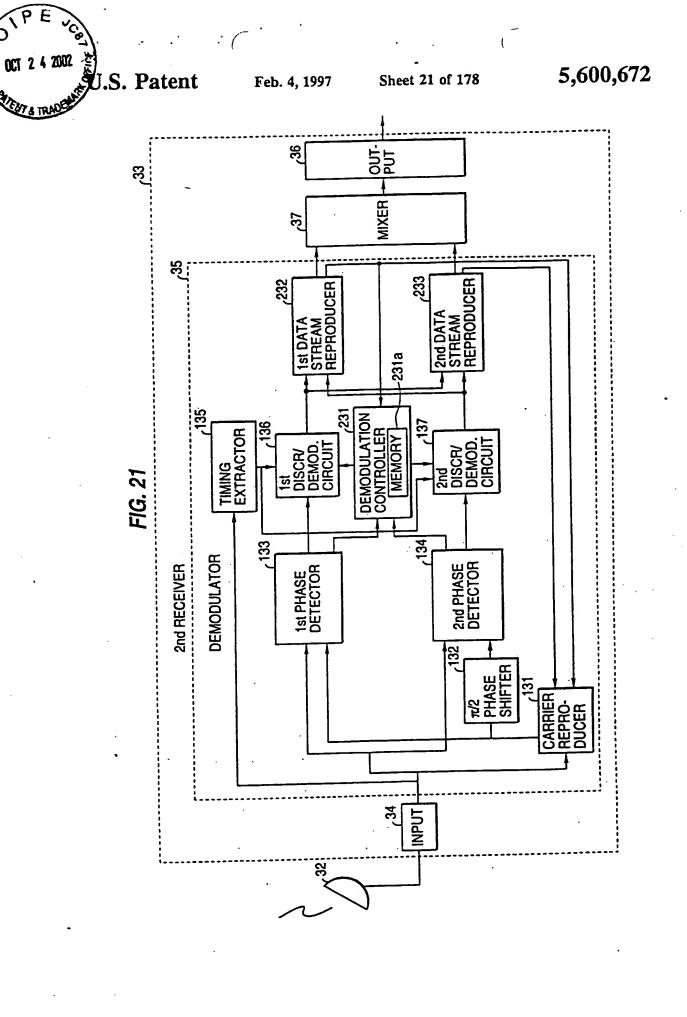
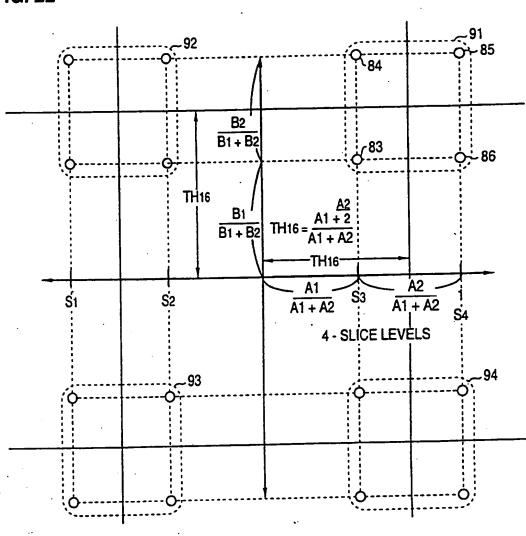


FIG. 22



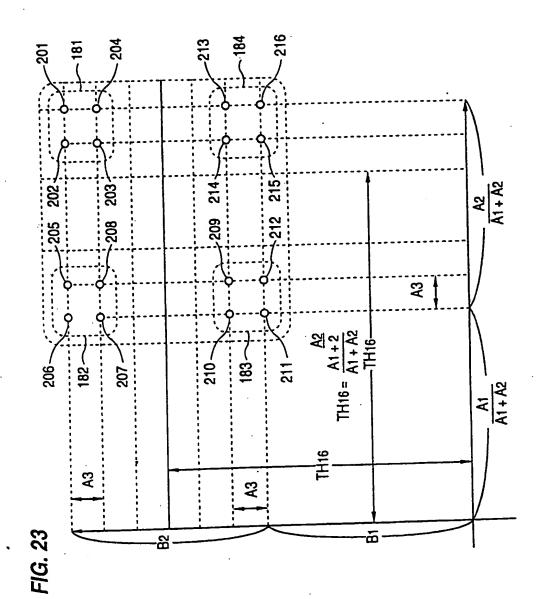
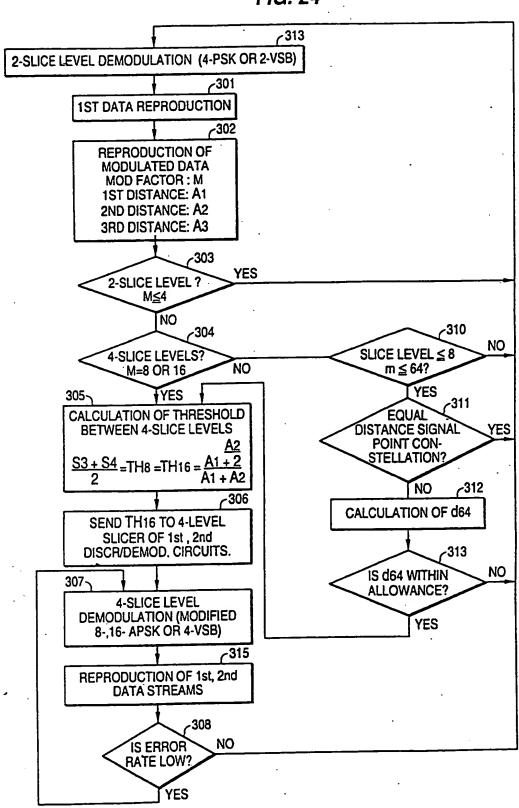
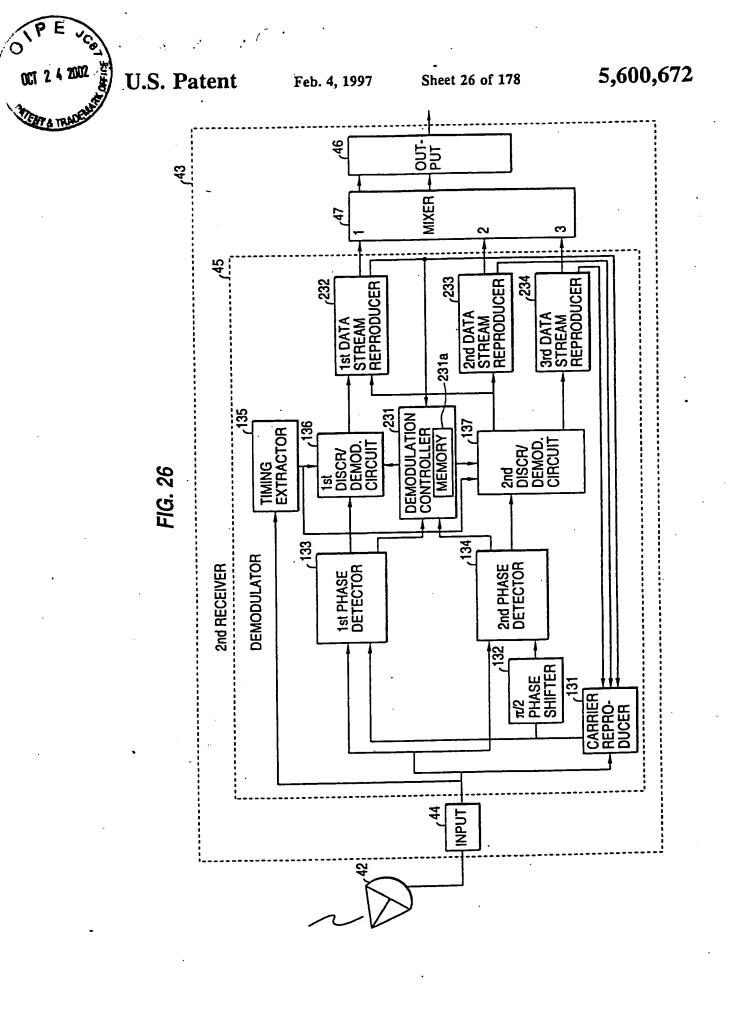
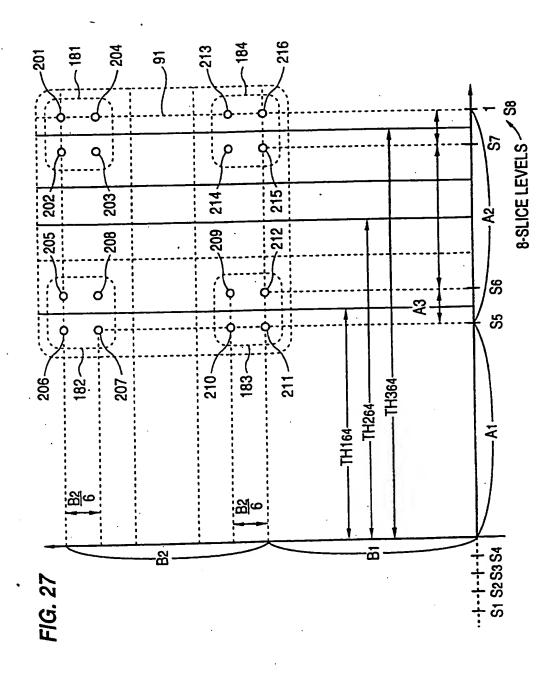


FIG. 24

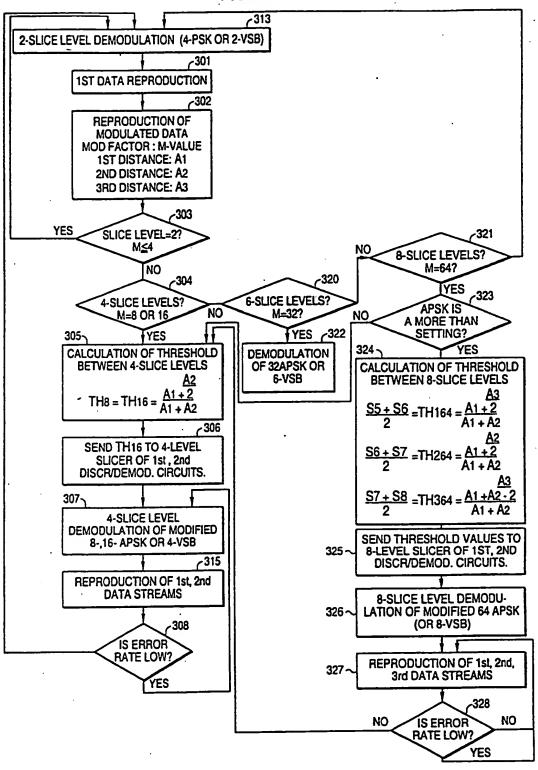


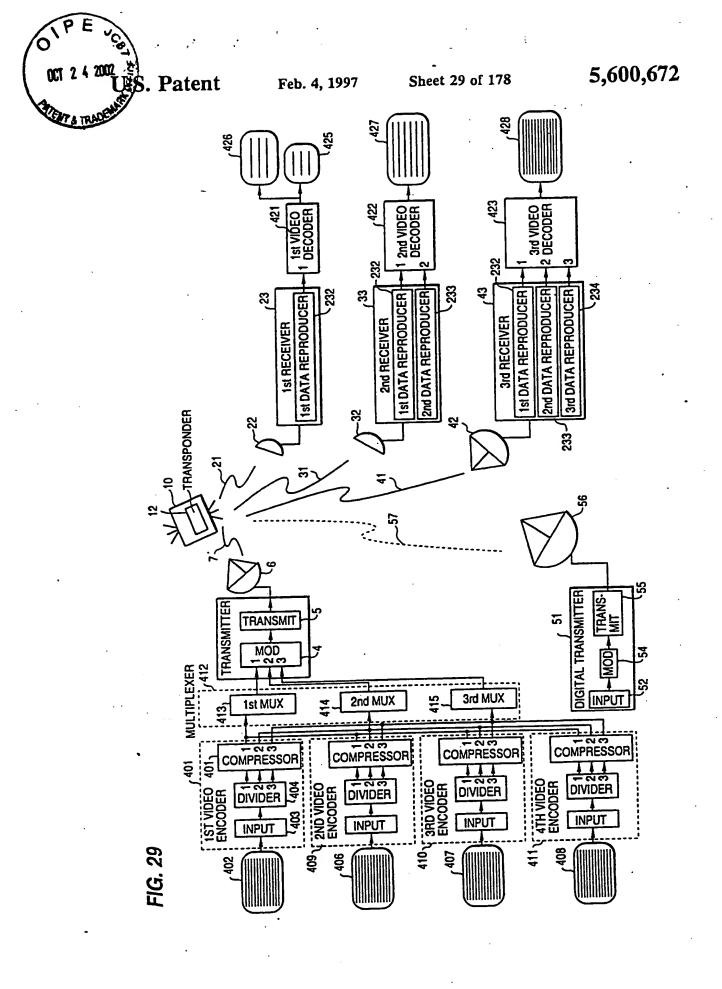




U.S. Patent

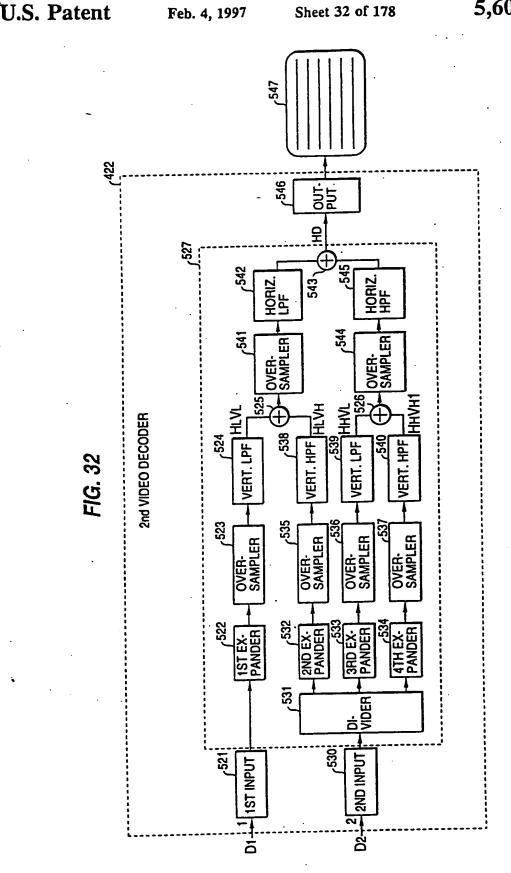
FIG. 28







OCT 2 4 2002



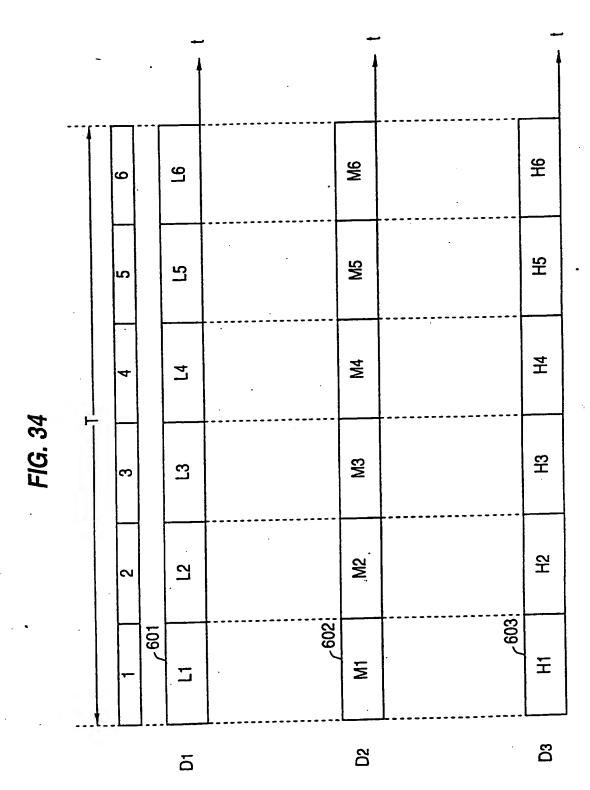
3 3RD INPUT

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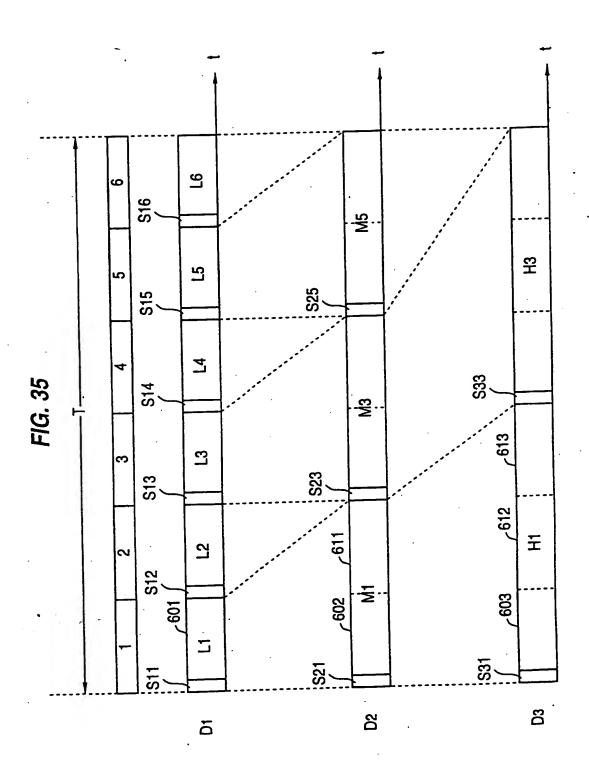
230

**1ST INPUT** 

D2 2 2ND INPUT



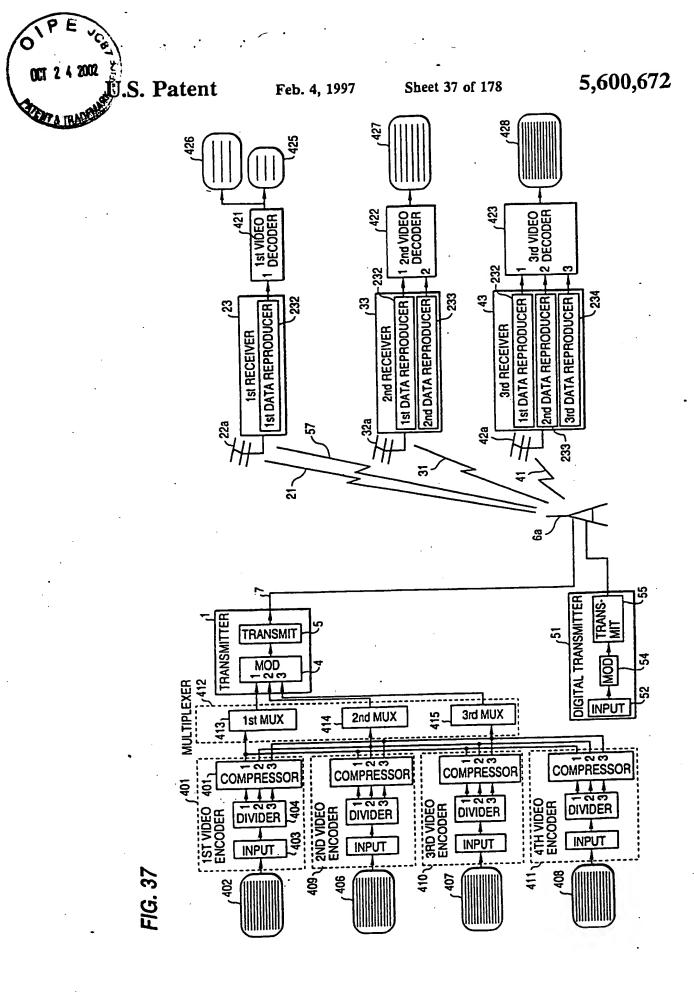
OCT 2 4 2002

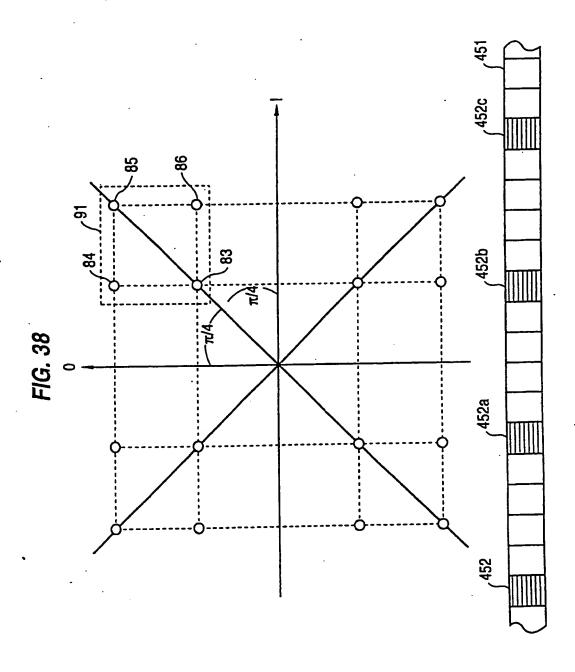


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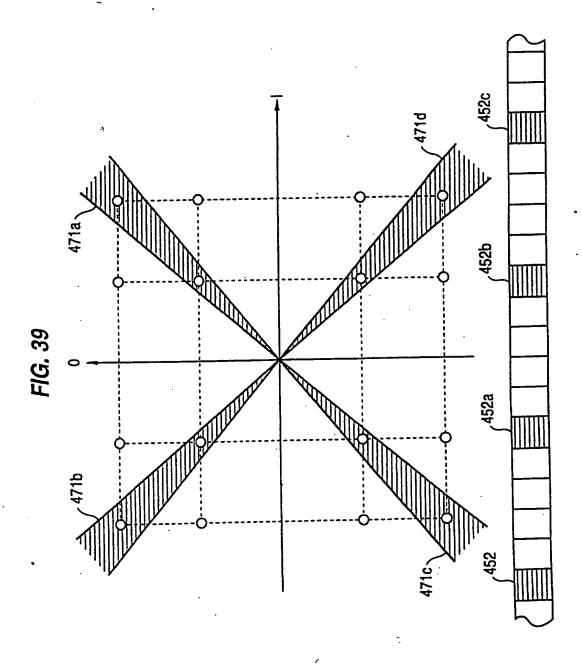
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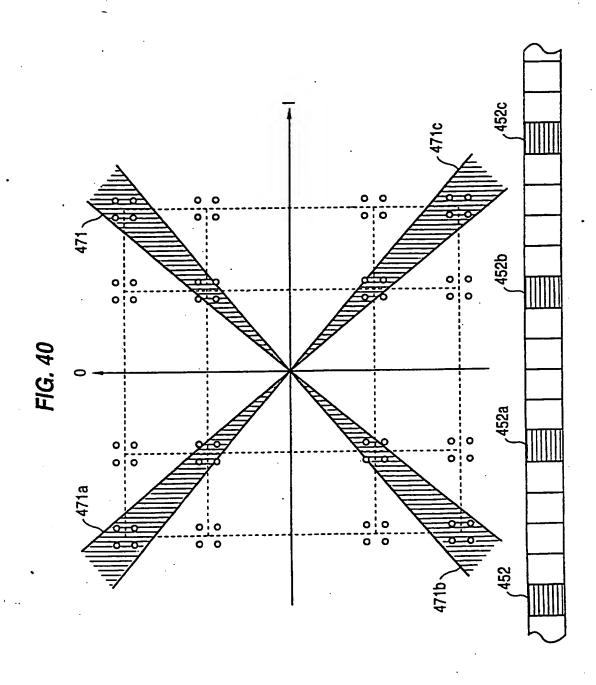


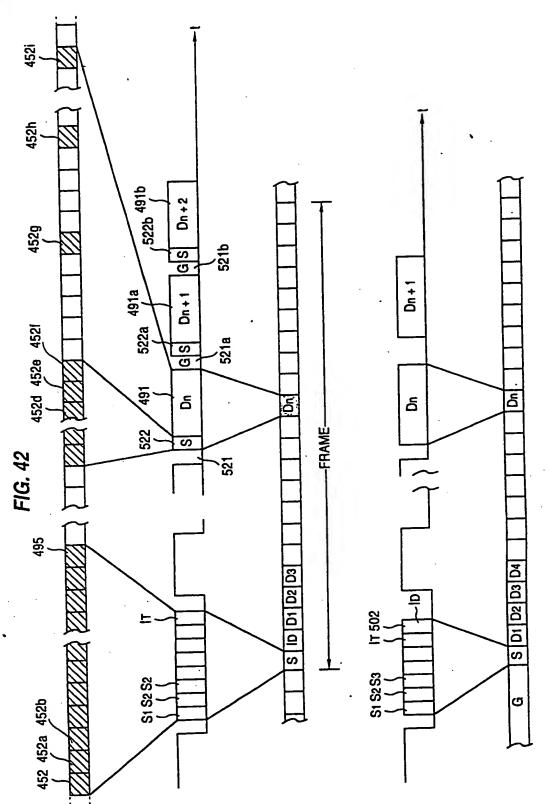


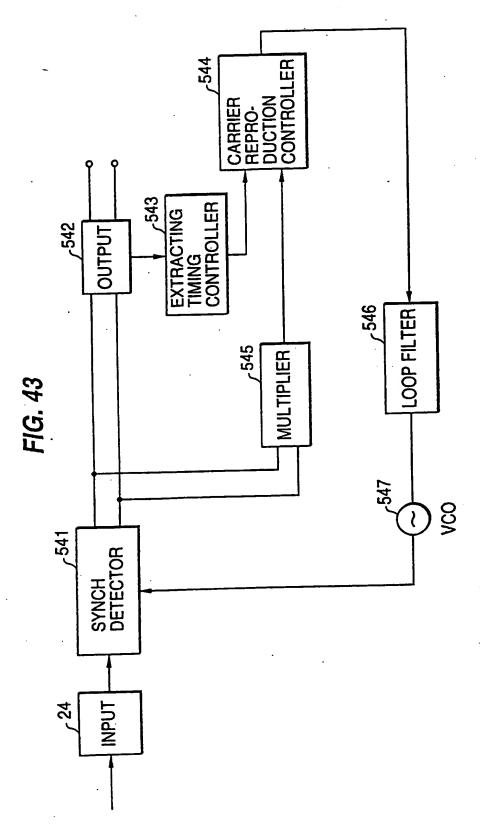


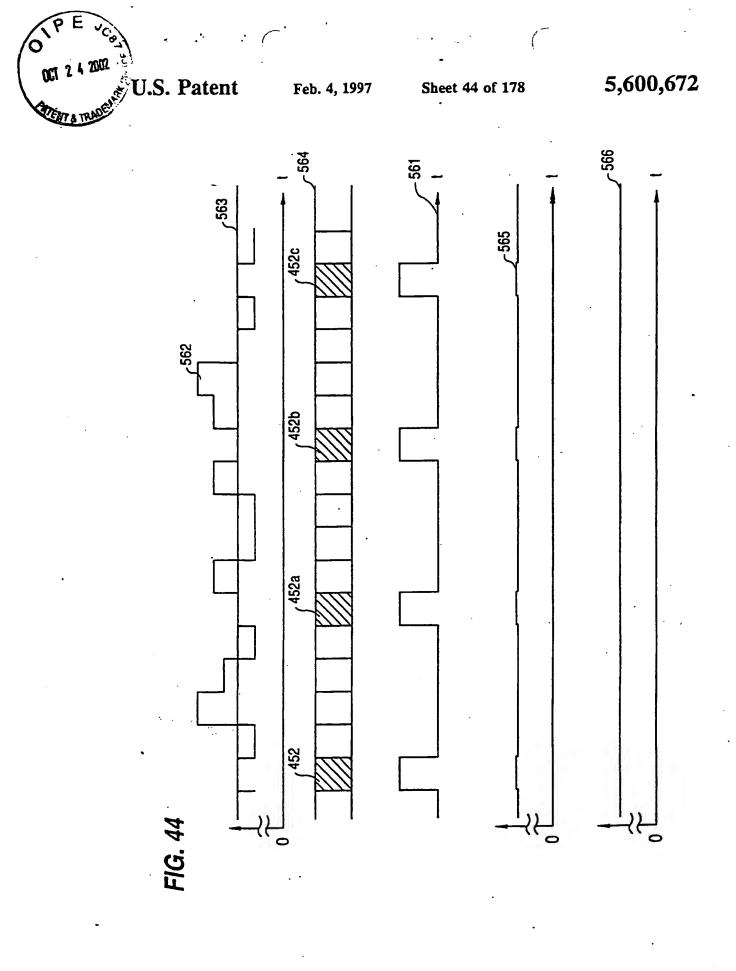


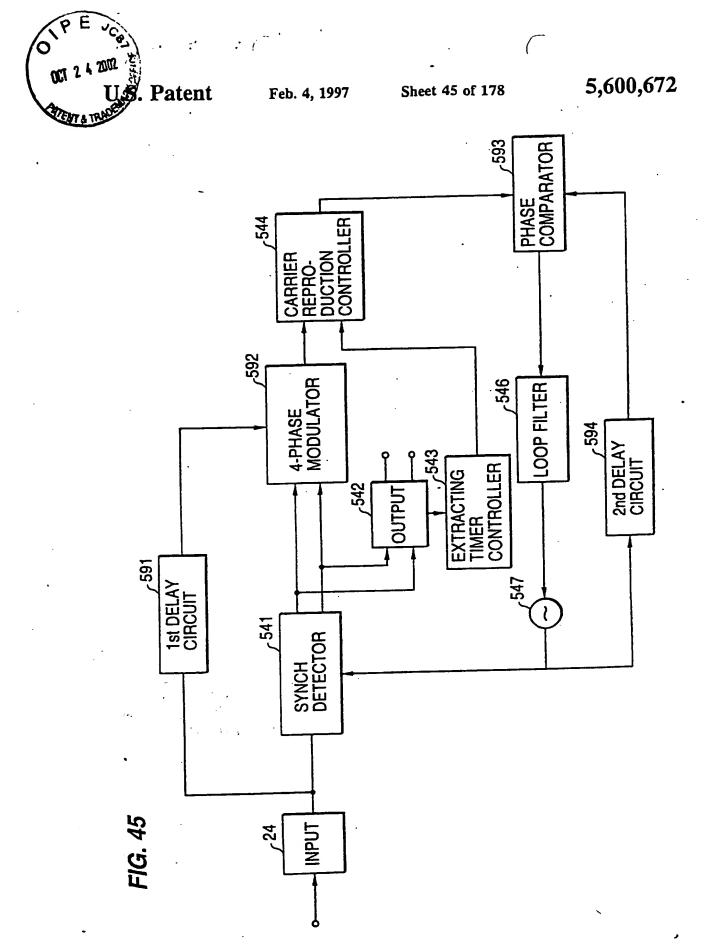
S. Patent Feb. 4, 19

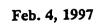


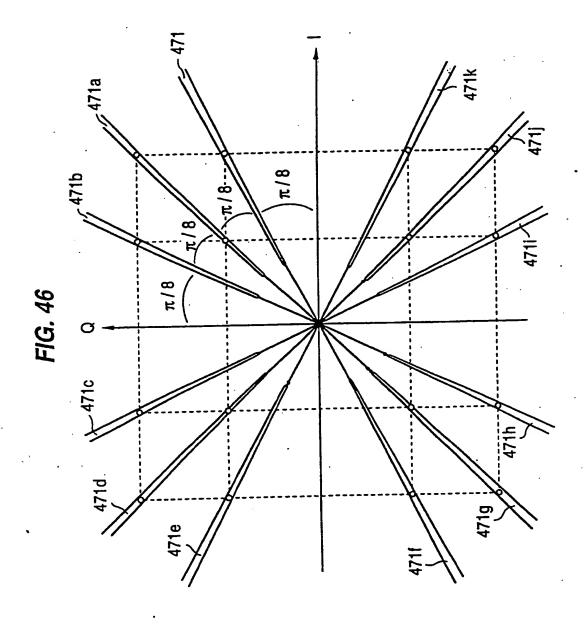




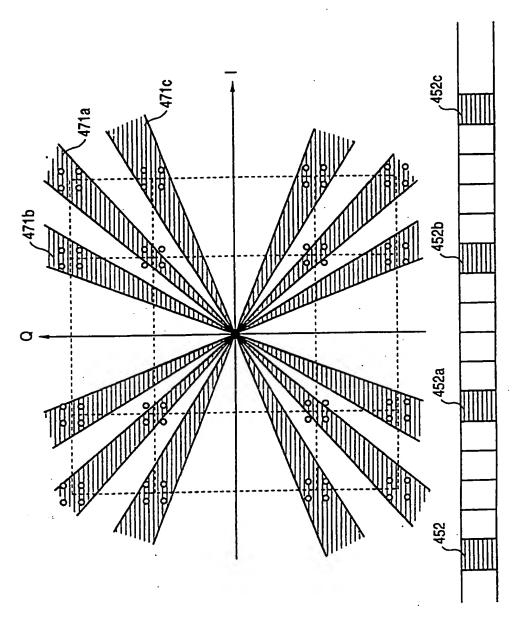




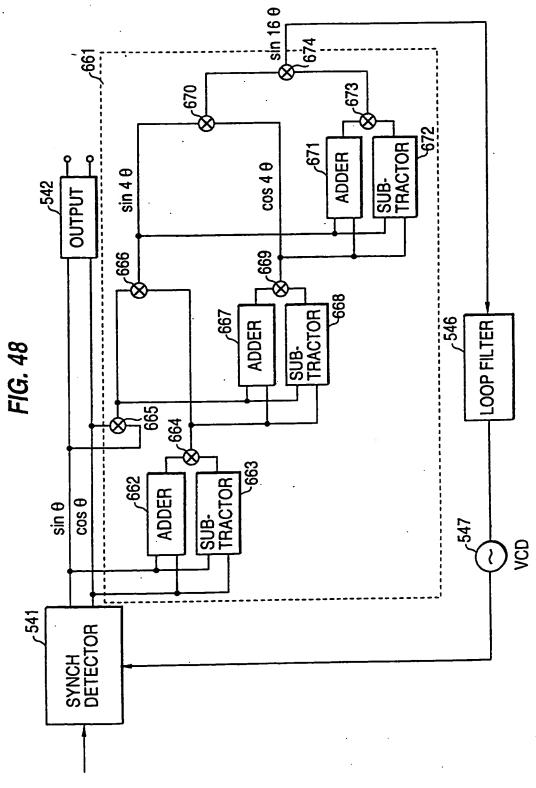






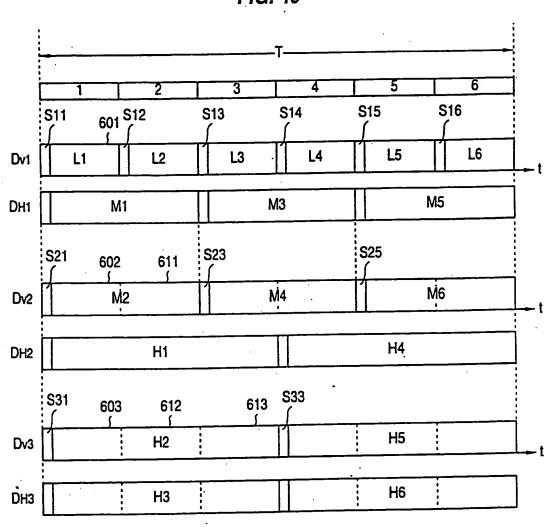




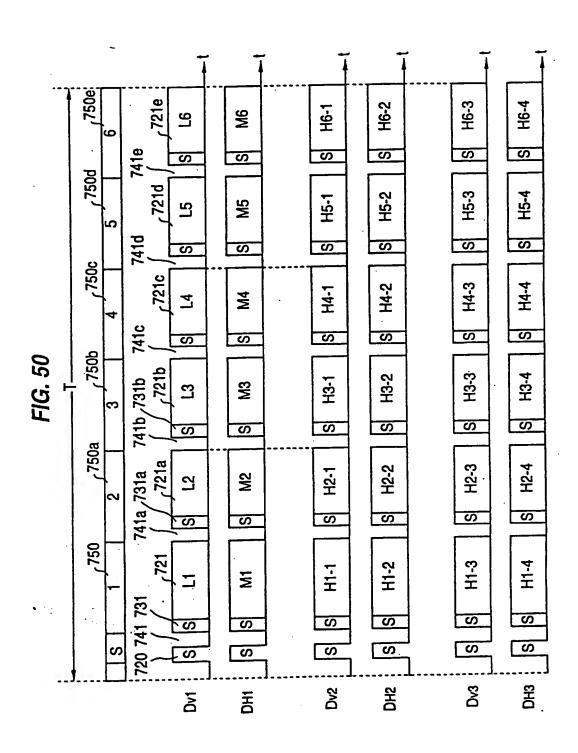


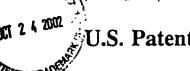
S. Patent

FIG. 49



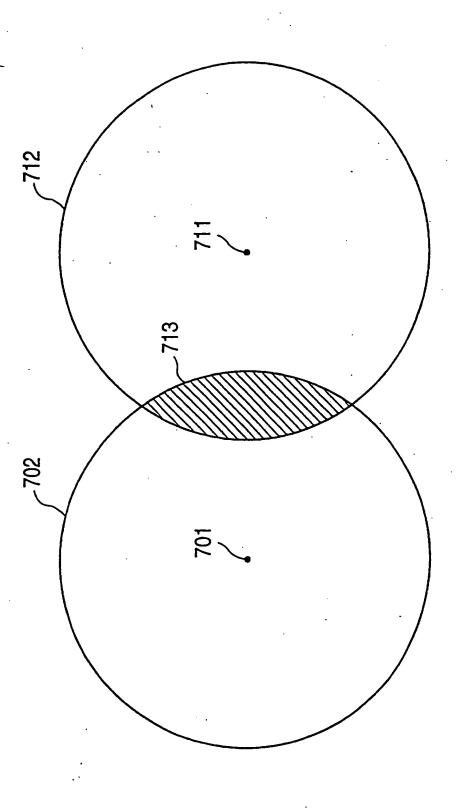


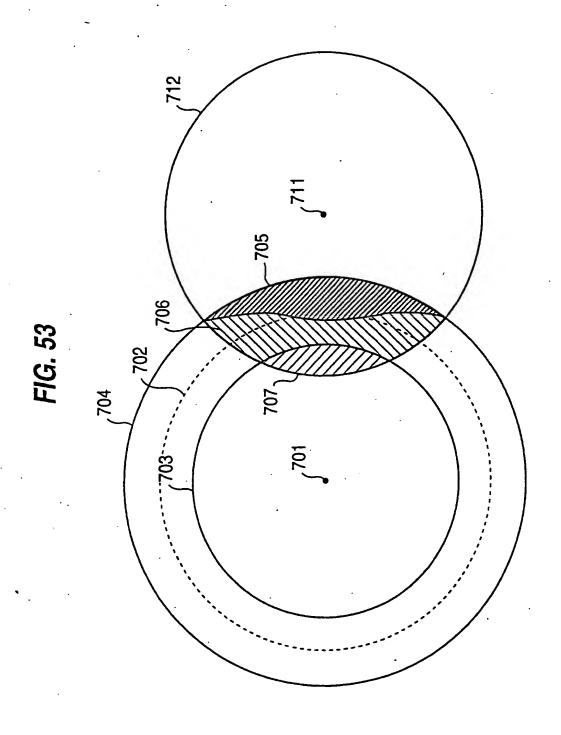




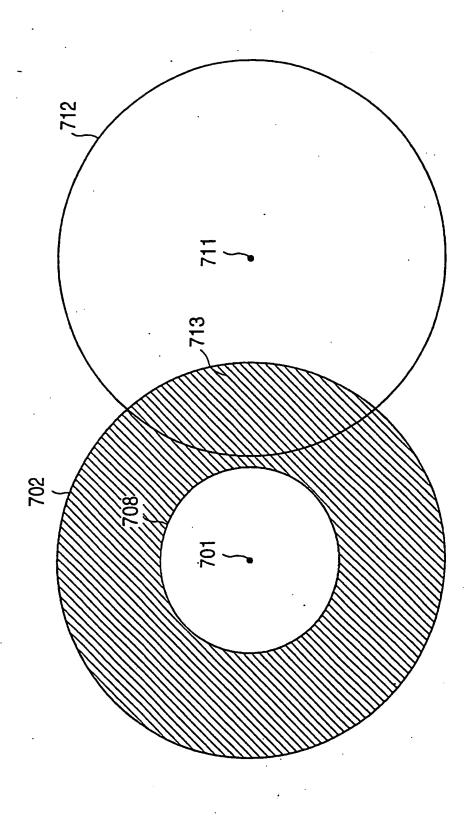
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7.0		2		S L5	S M5-1		S M5-2	S M5-3		S H5-1	S H5-2
		4		S L4	S M4-1		S M4-2	S M4-3		S H4-1	S H4-2
	1	က		S S	S M3-1		S M3-2	S M3-3		S H3-1	S H3-2
FIG. 51		2		S [2	S M2-1		S M2-2	S M2-3		S H2-1	S H2-2
		-	,601	S L1	S M1-1		S M1-2	S M1-3		S H1-1	S H1-2
		<u>s</u>		<u>s</u>	DH1		Dvz	DH2	1	S S	DH3

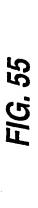


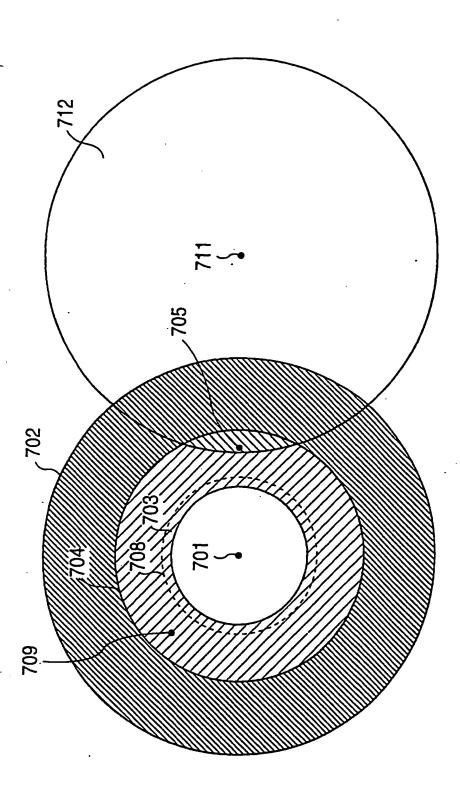




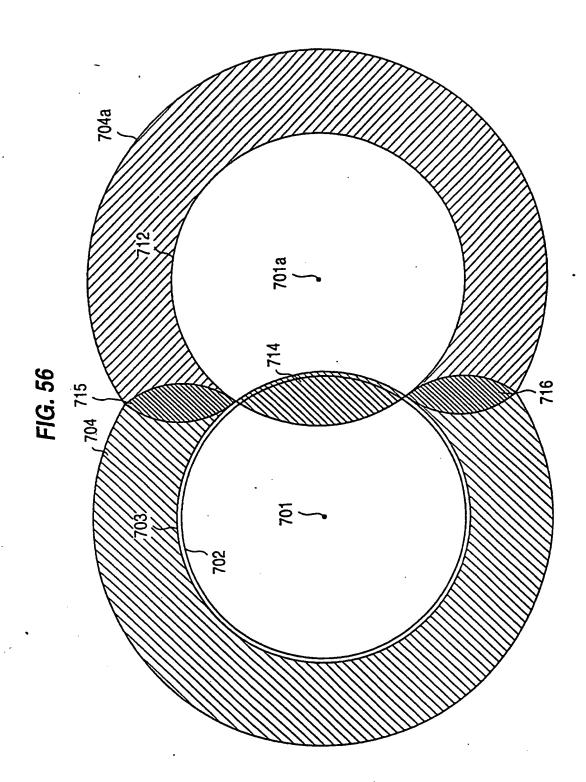


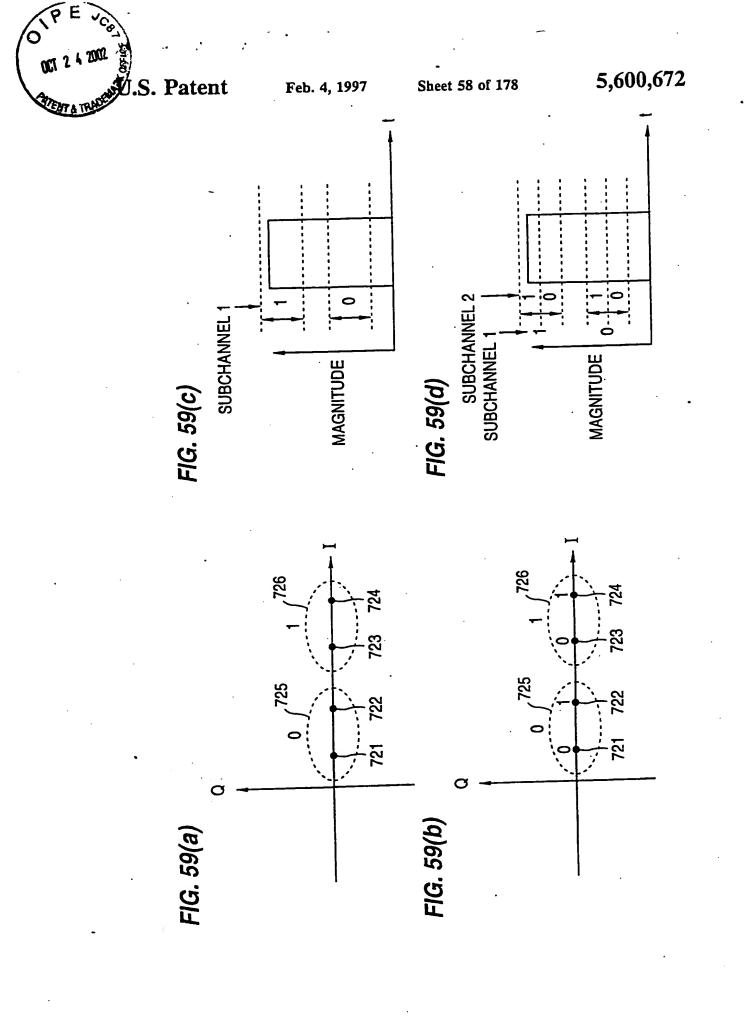












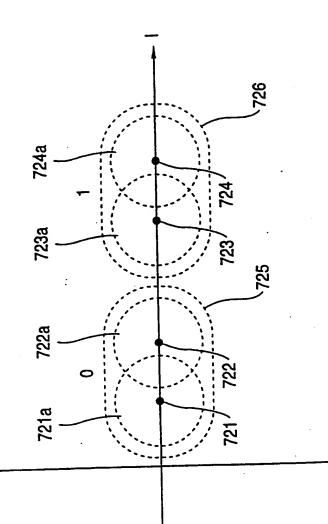
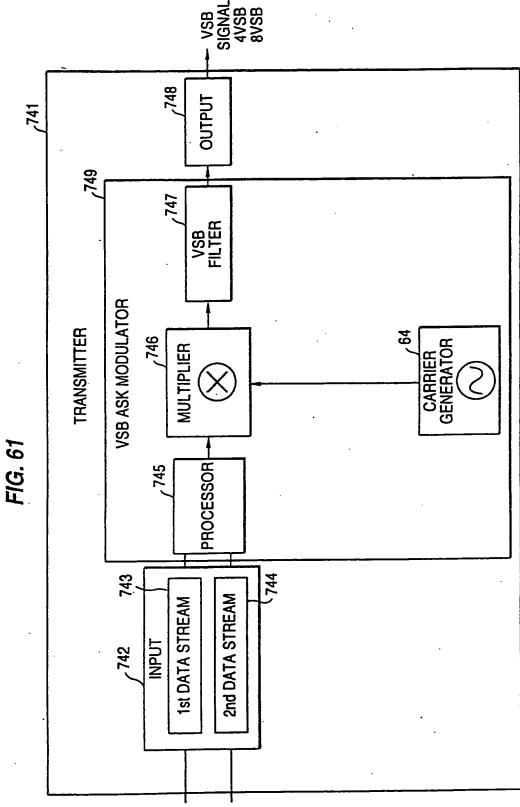
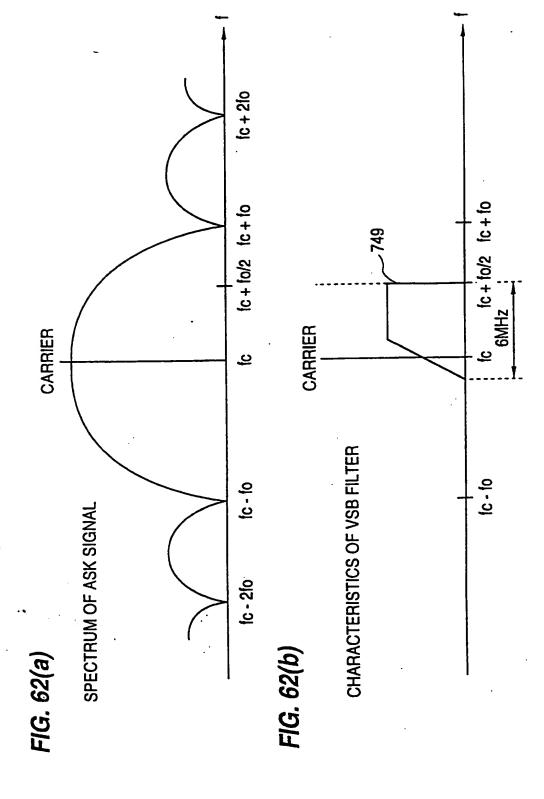
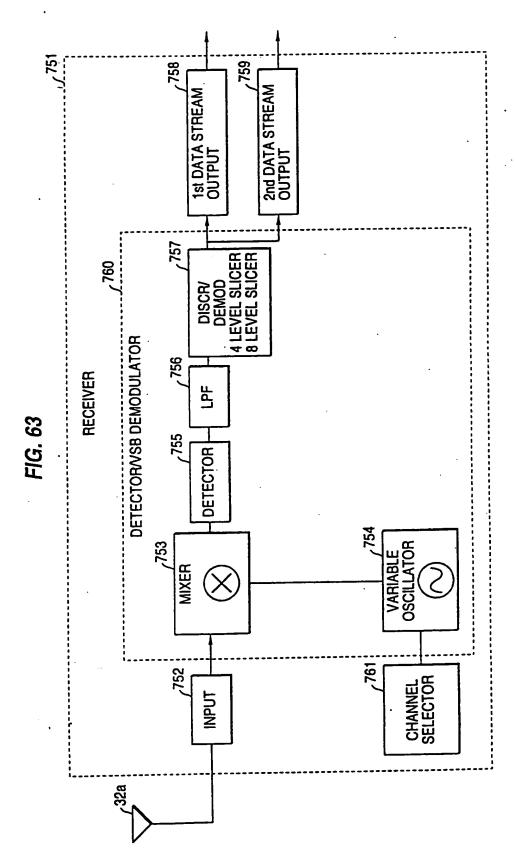


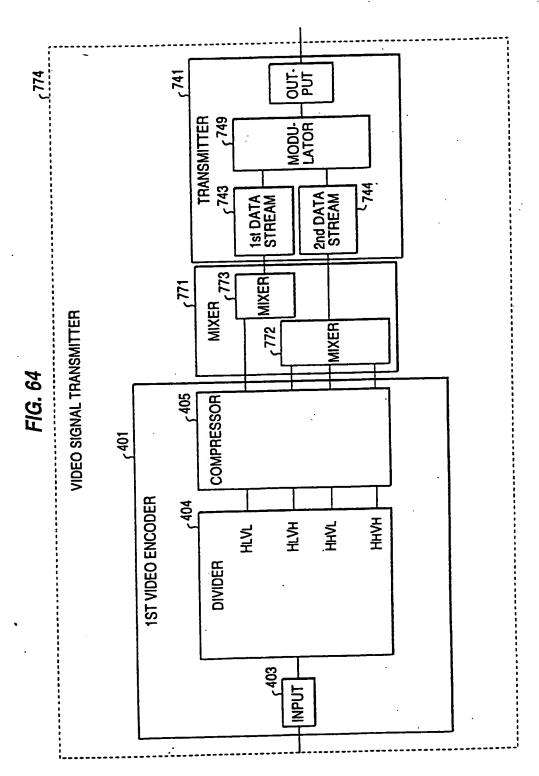
FIG. 60

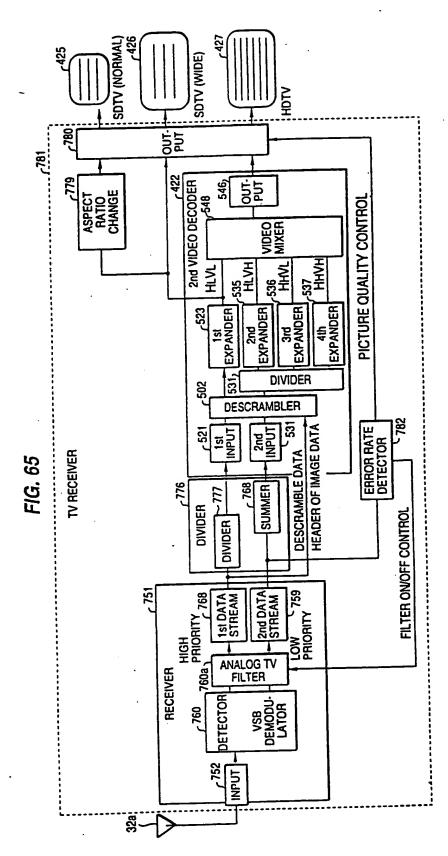


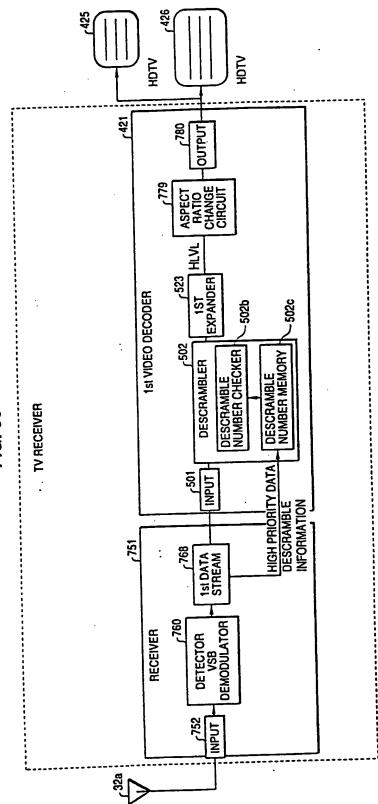






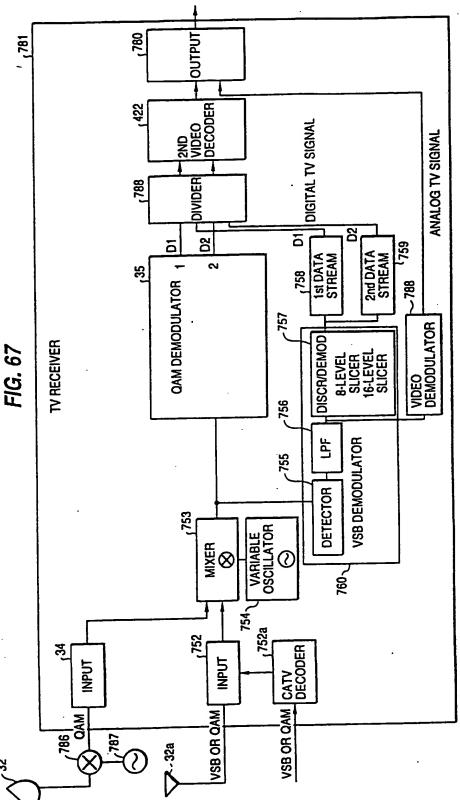


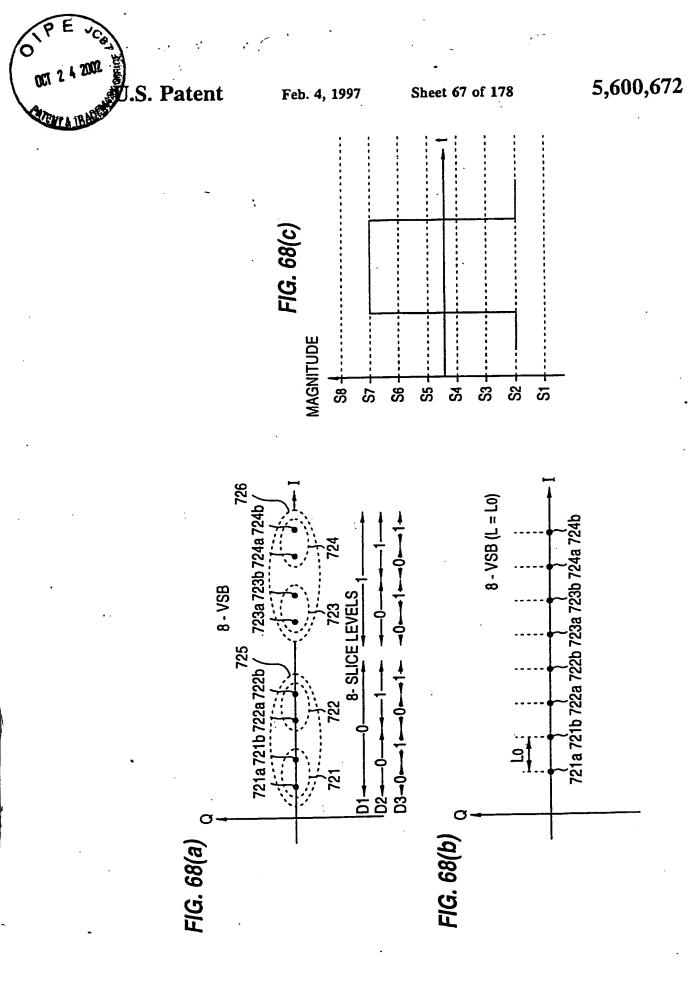


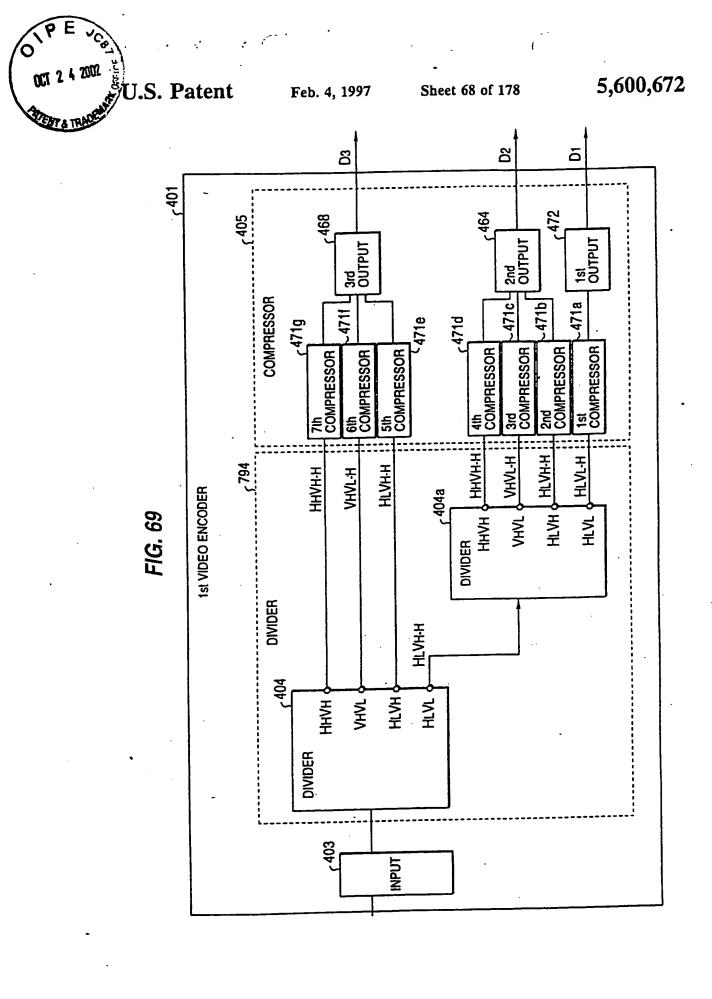


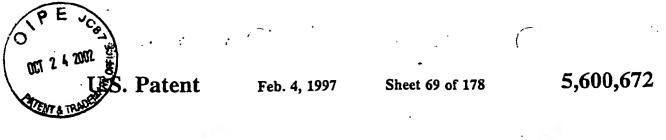
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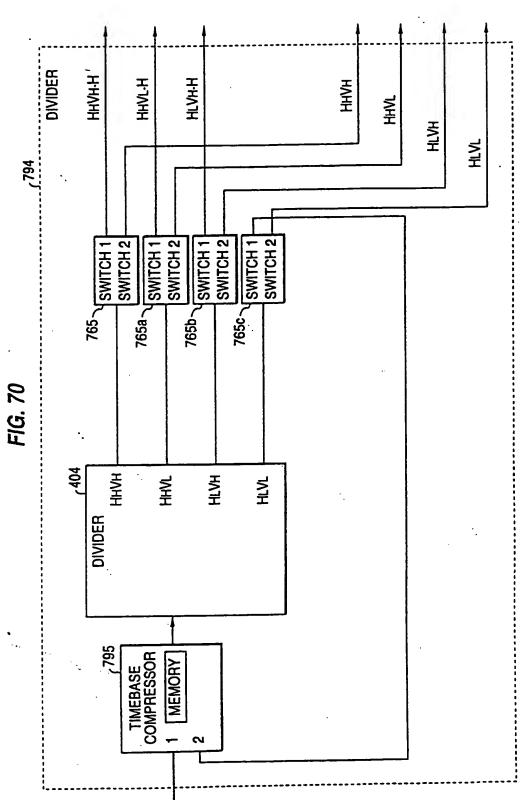












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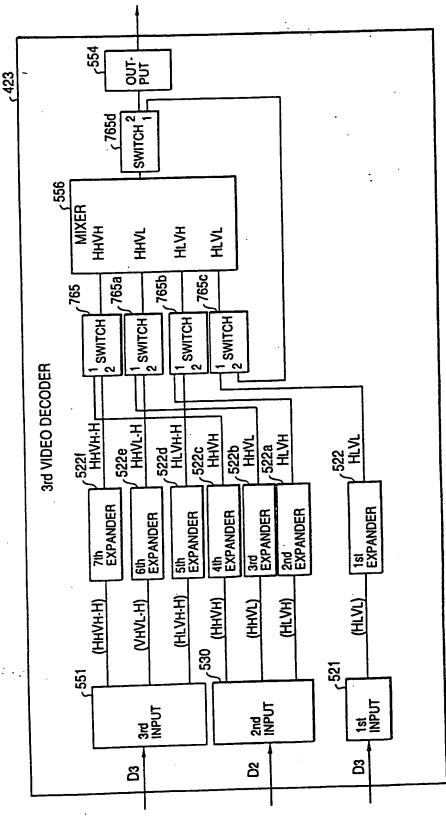


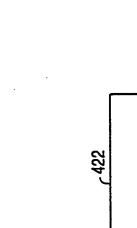
FIG. 72

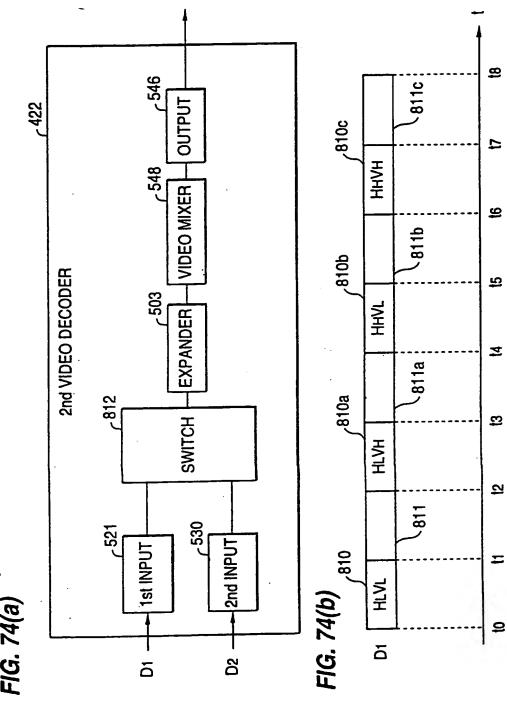


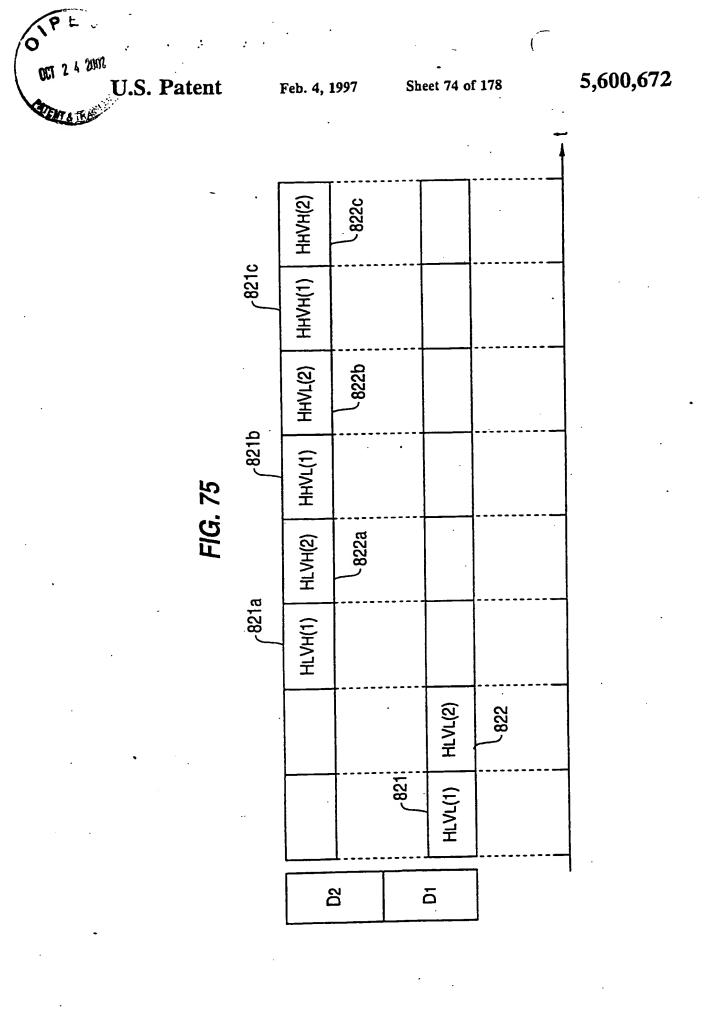
Sheet 72 of 178

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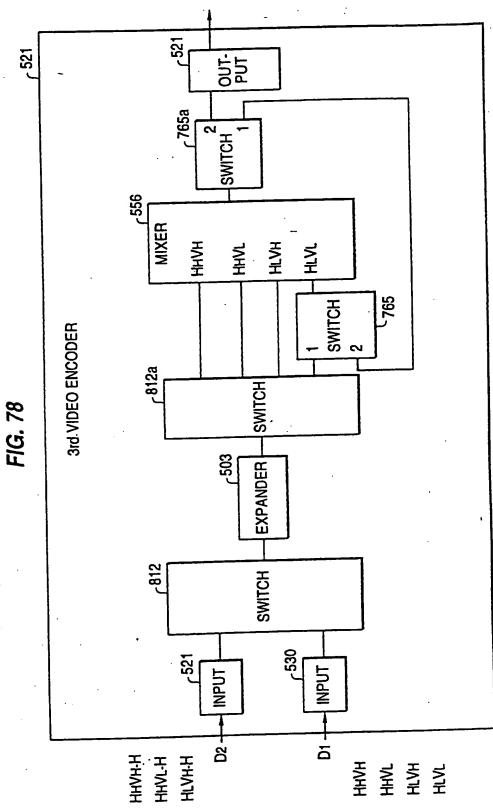
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		D1 HLVL HLVH HHVL HHVH	HLVL HLVH HHVL HHVH TIMING 1



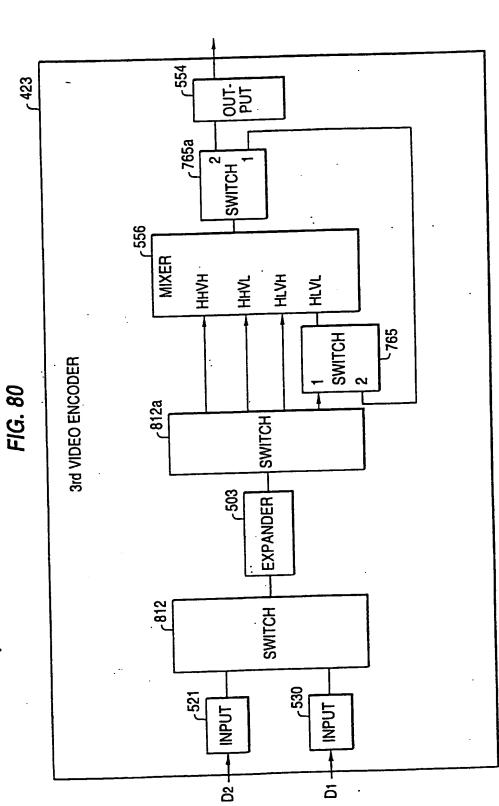


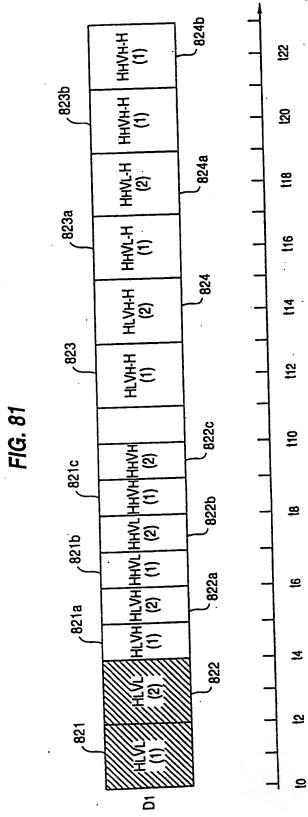


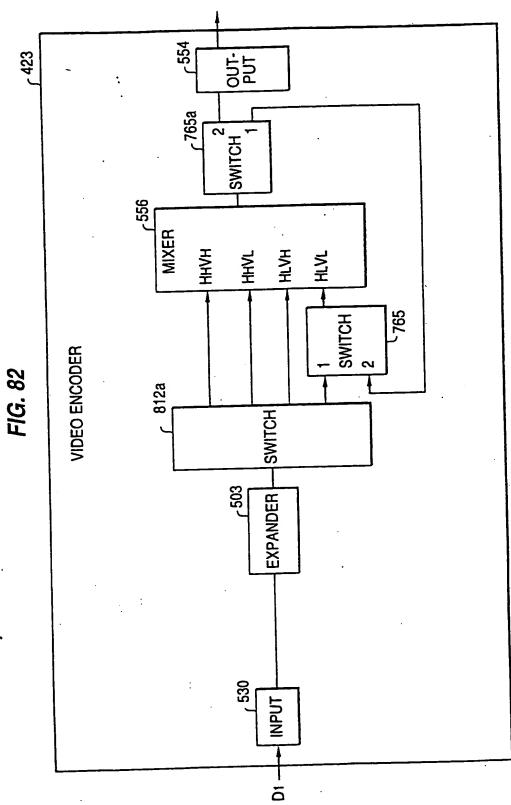
			+	
6821 (821a (821c	НнVн(2)	\ 822c		
	ННVH(1) ННVH(2)			
	ННVL(2)	\ 822b		01-2
	HHVL(1)			0
	НгVн(2)	\ 822a		
	HLVH(1)			
	HLVL(2)	622		-
	HLVL(1) HLVL(2)			1-10
	٥		Ì	ECC

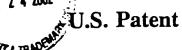


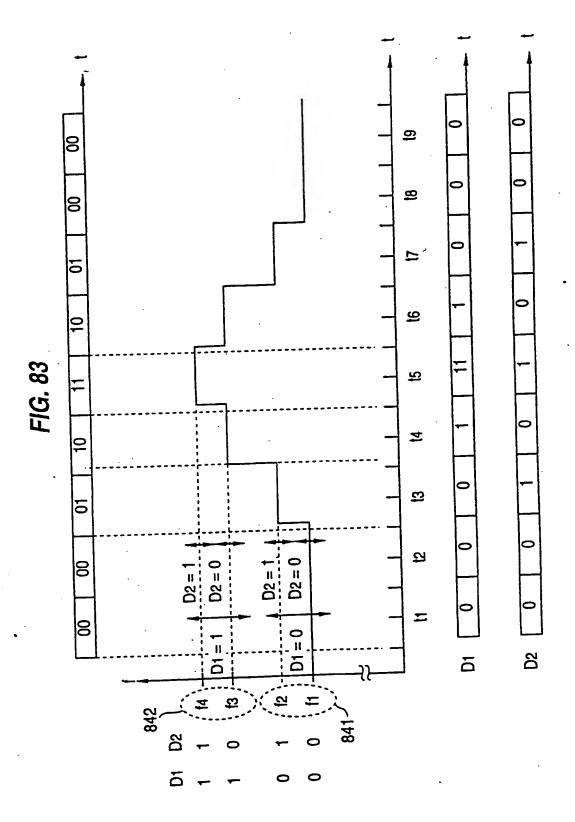


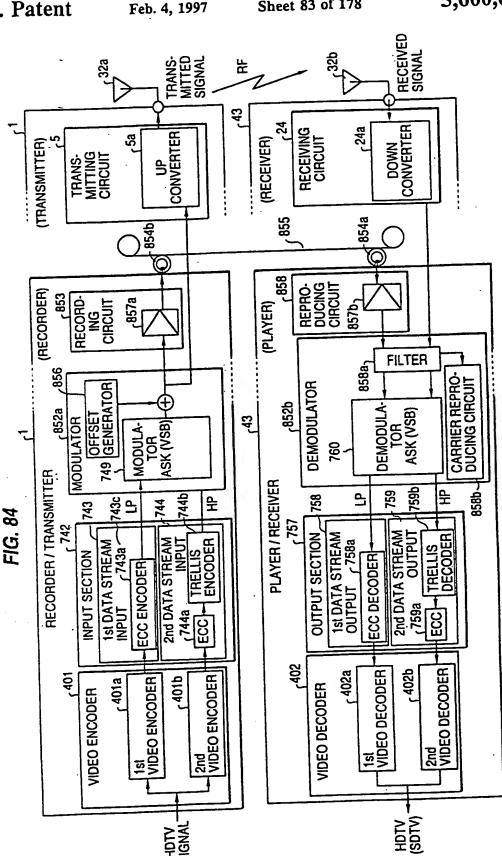












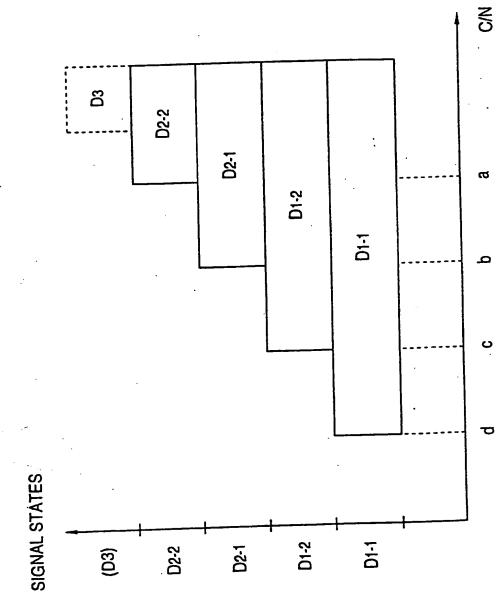
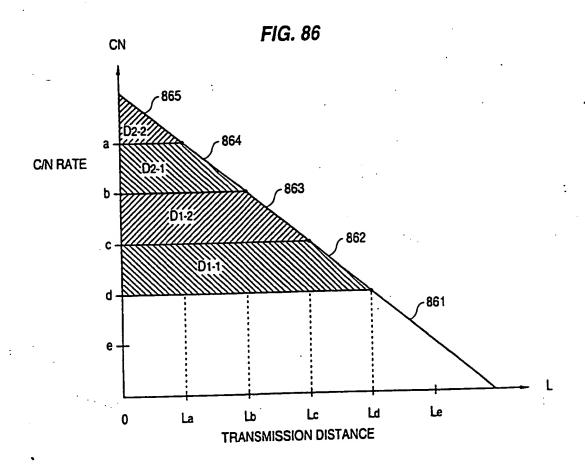
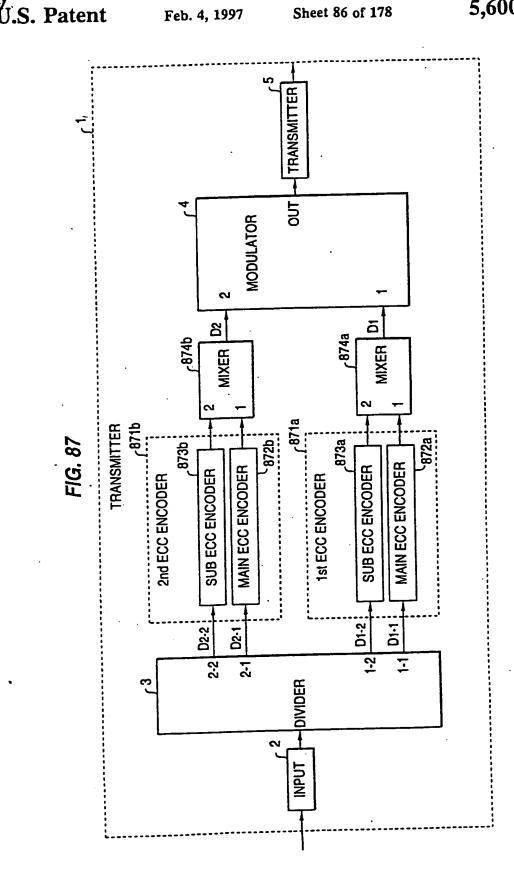


FIG. 85





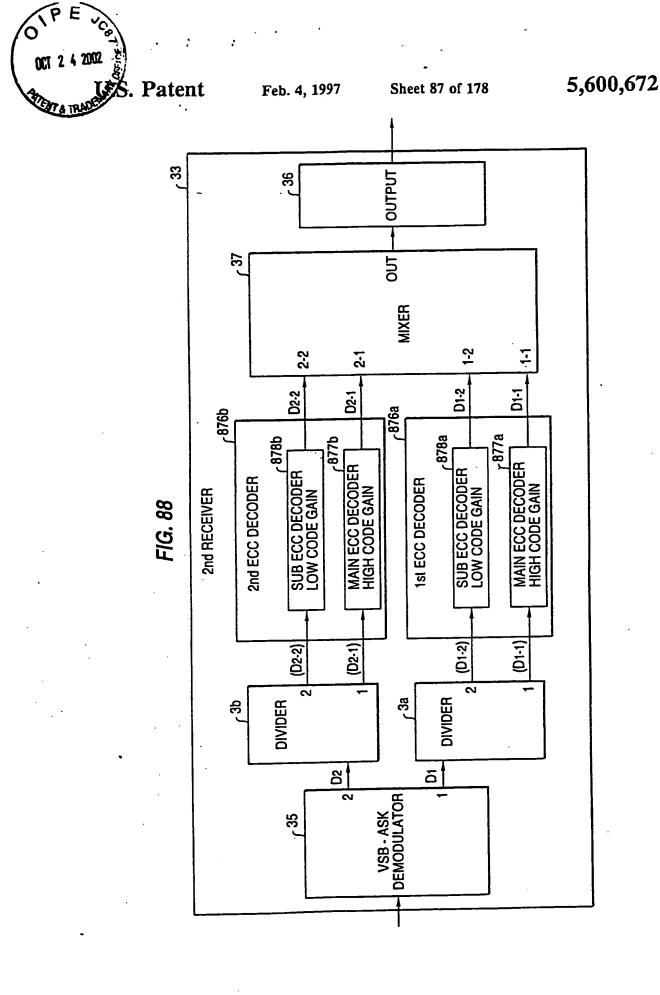
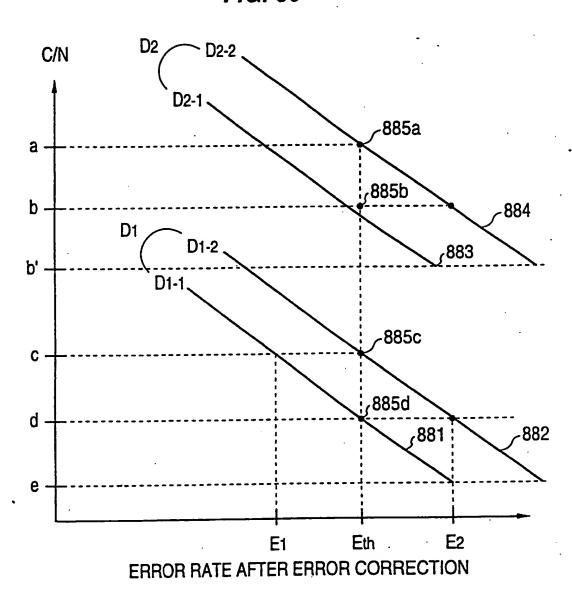
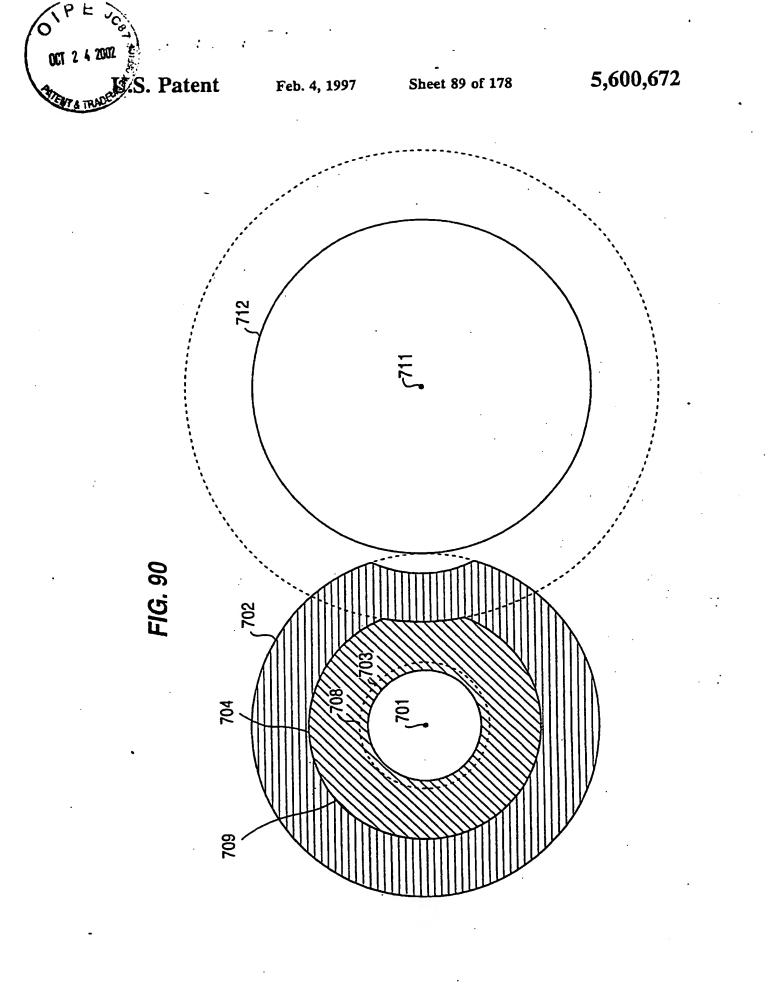




FIG. 89





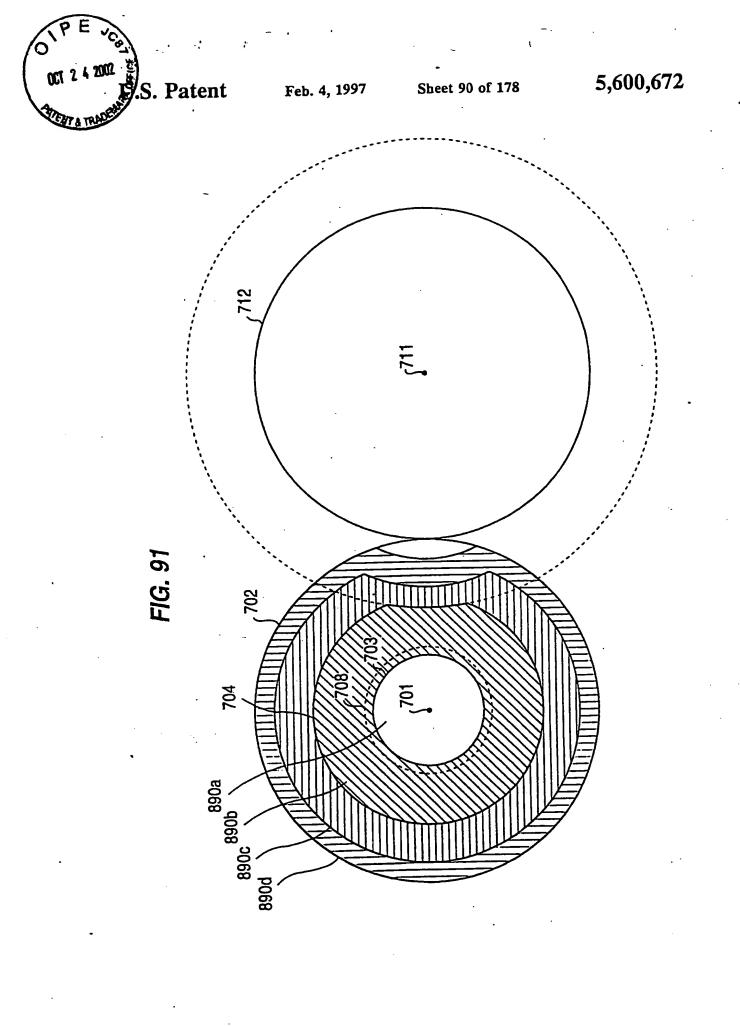
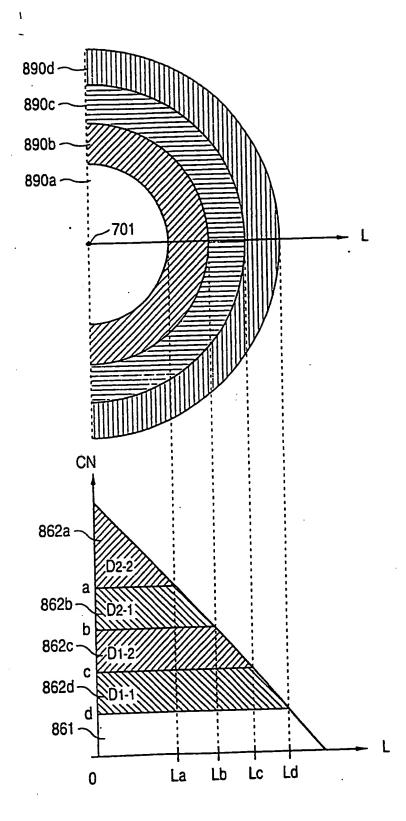
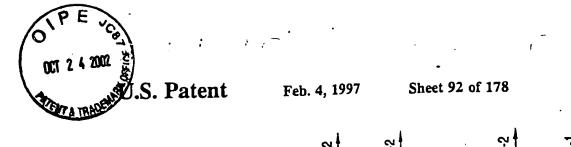
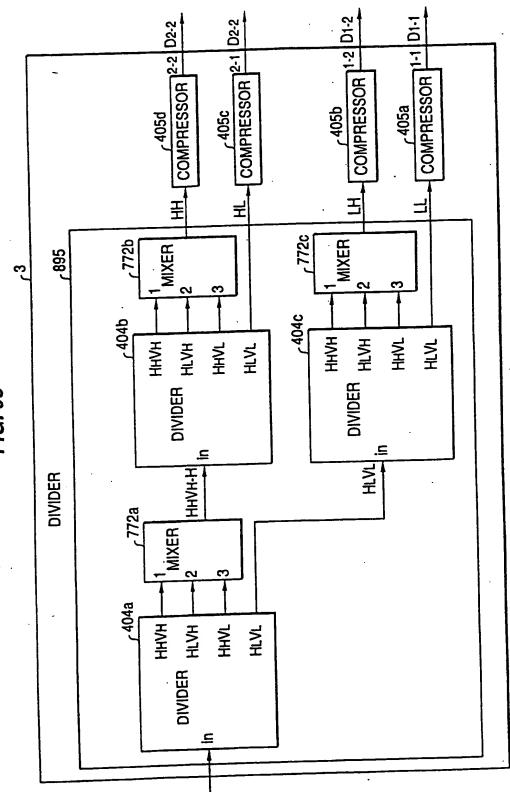


FIG. 92









Feb. 4, 1997 **Patent** 

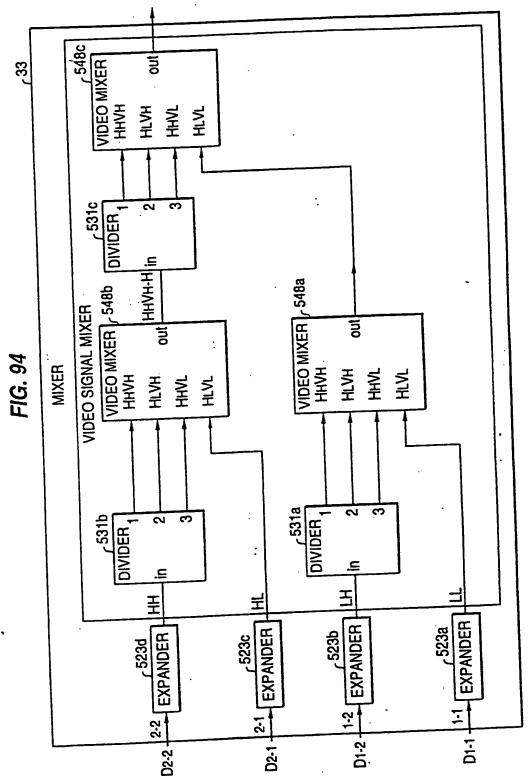
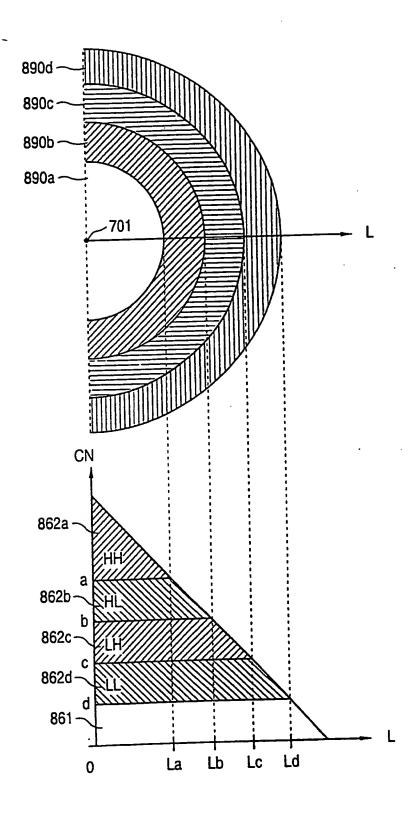
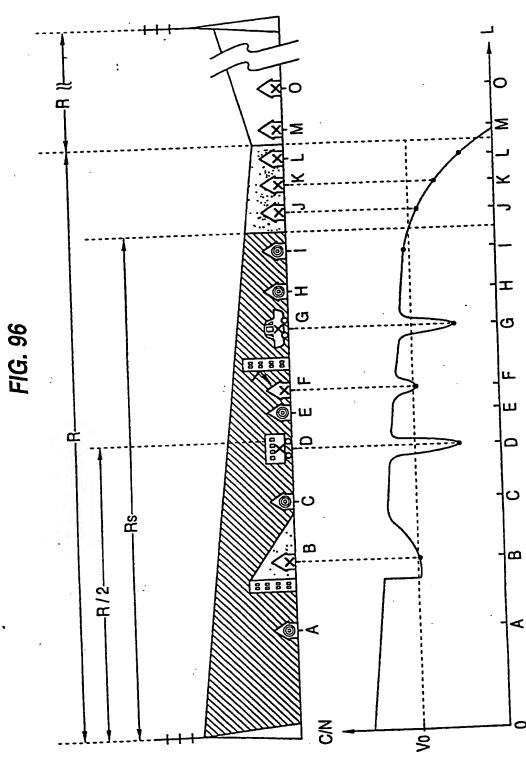




FIG. 95





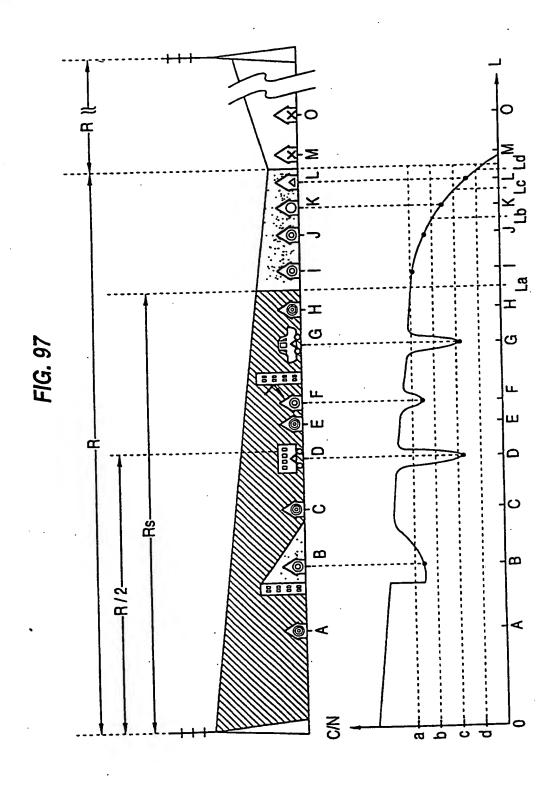
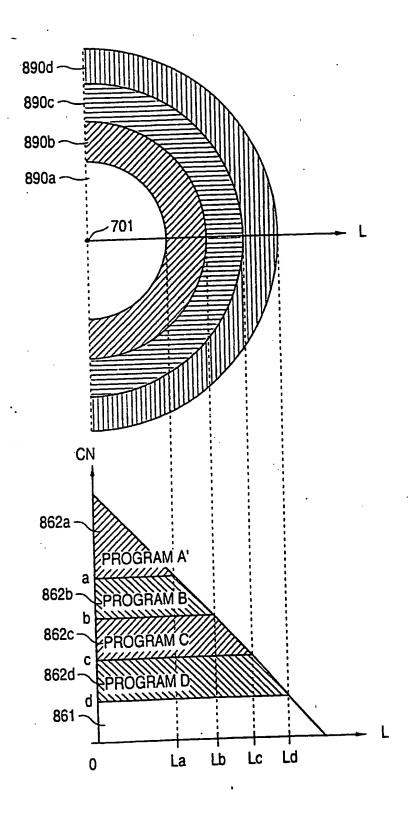


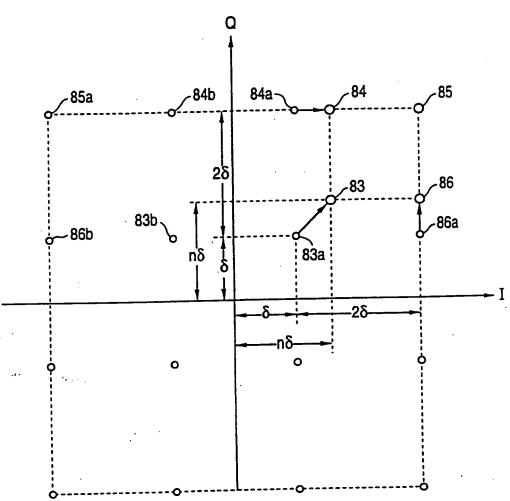
FIG. 98



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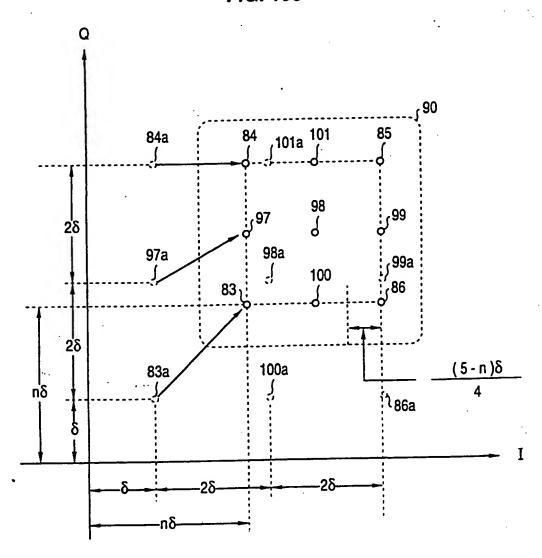
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FIG. 99



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FIG. 100



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FIG. 101

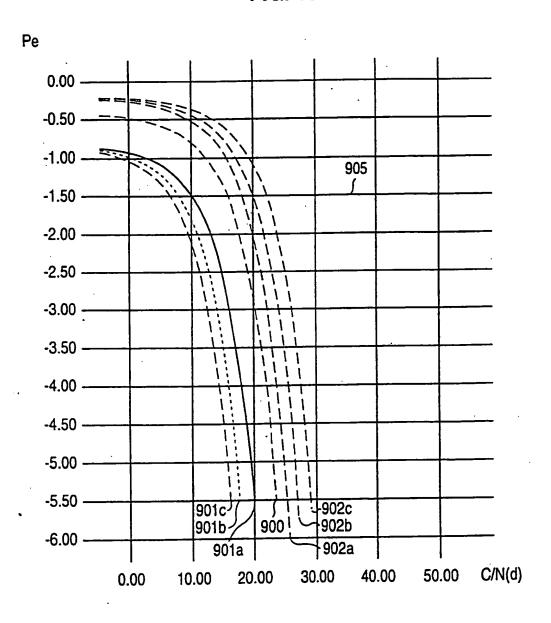
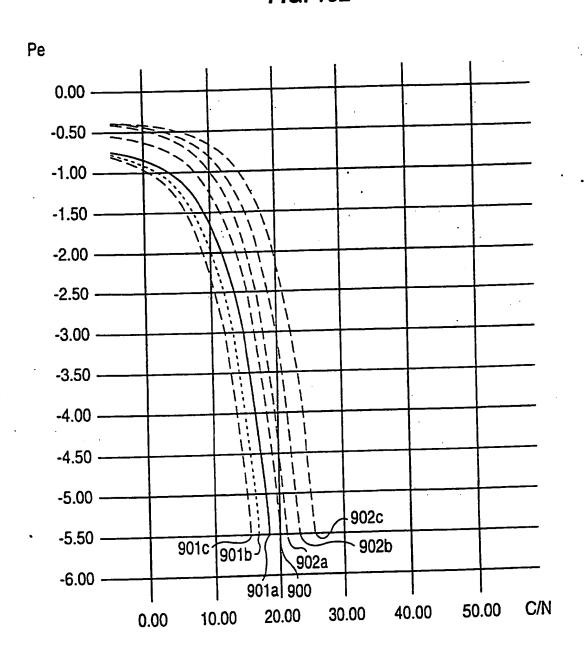




FIG. 102



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FIG. 103

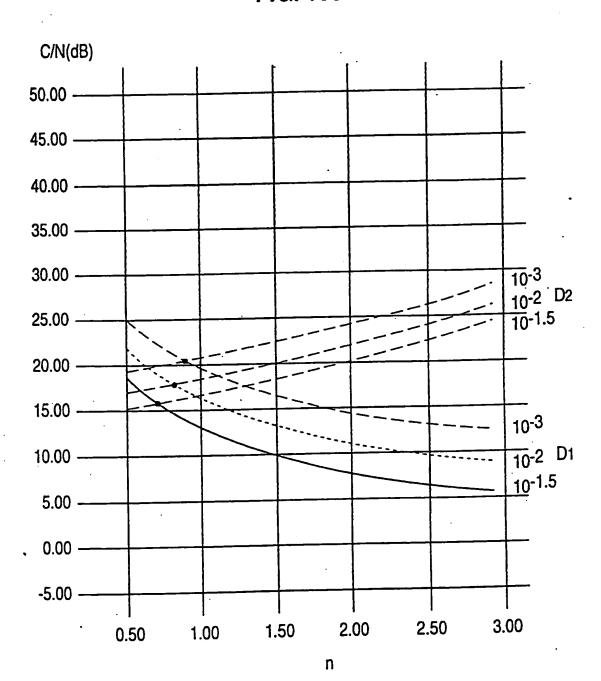
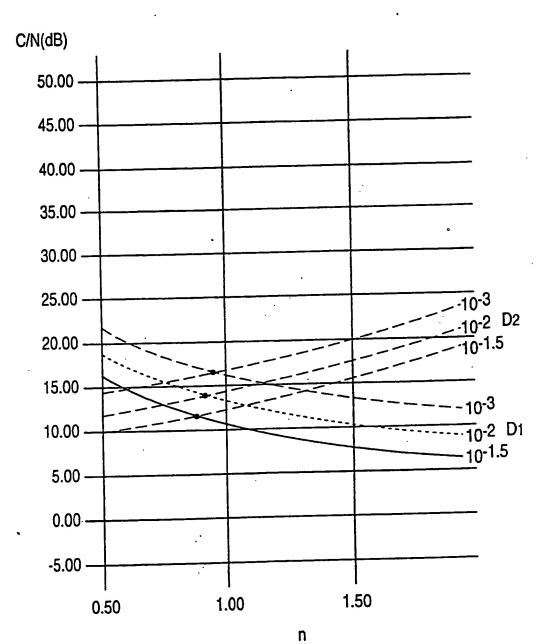
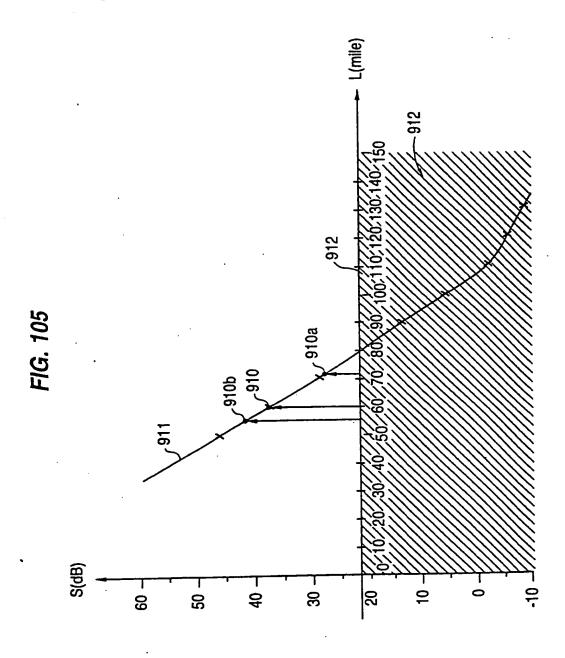
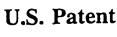
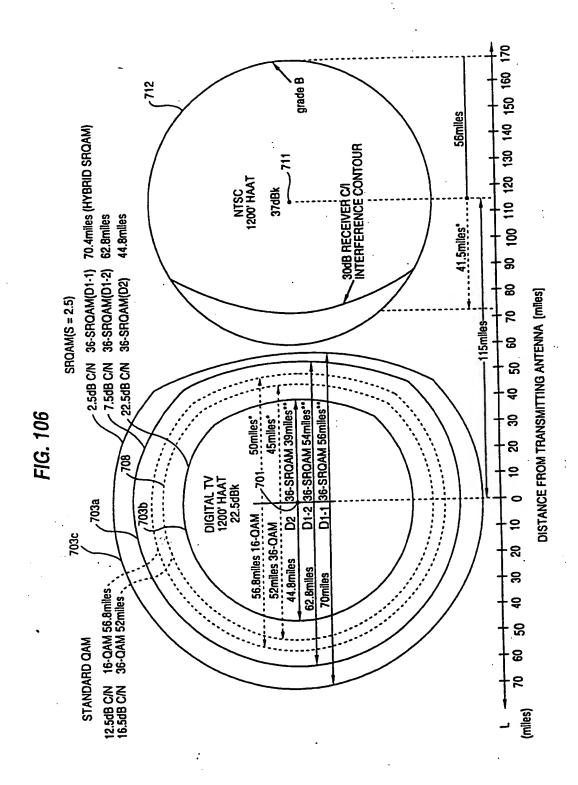


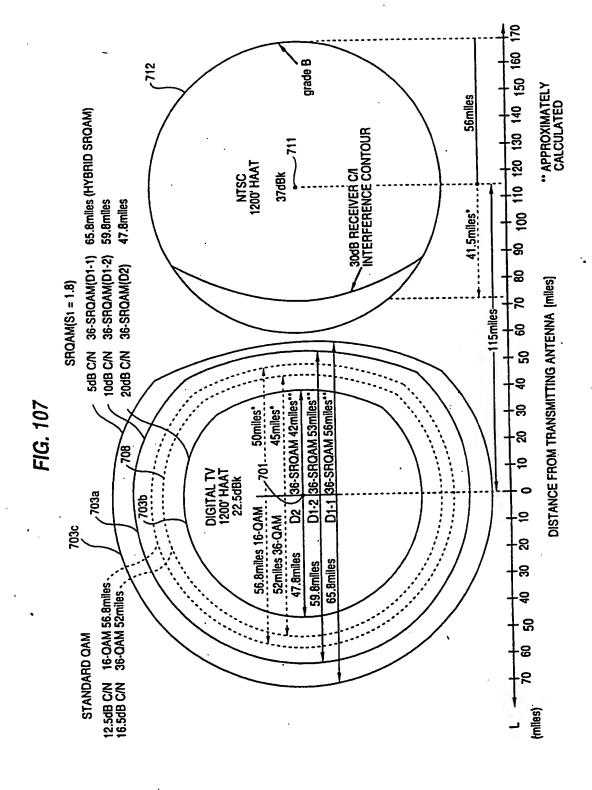
FIG. 104



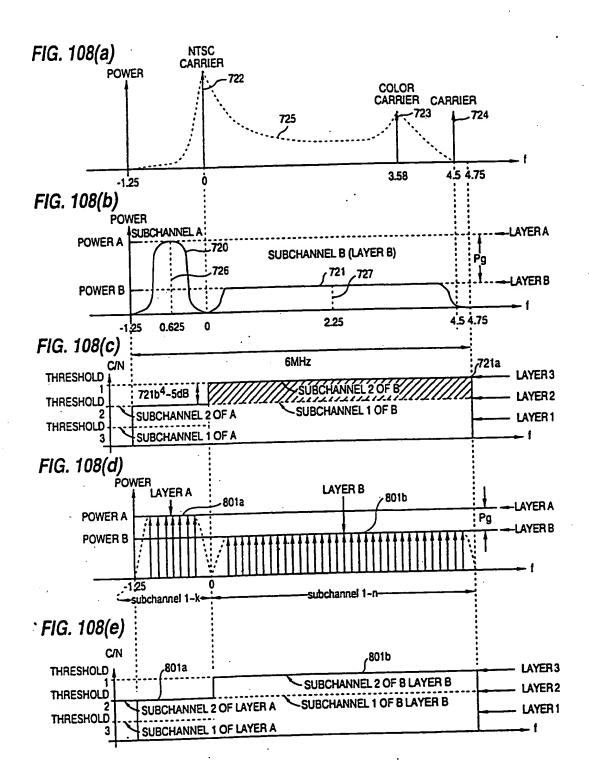








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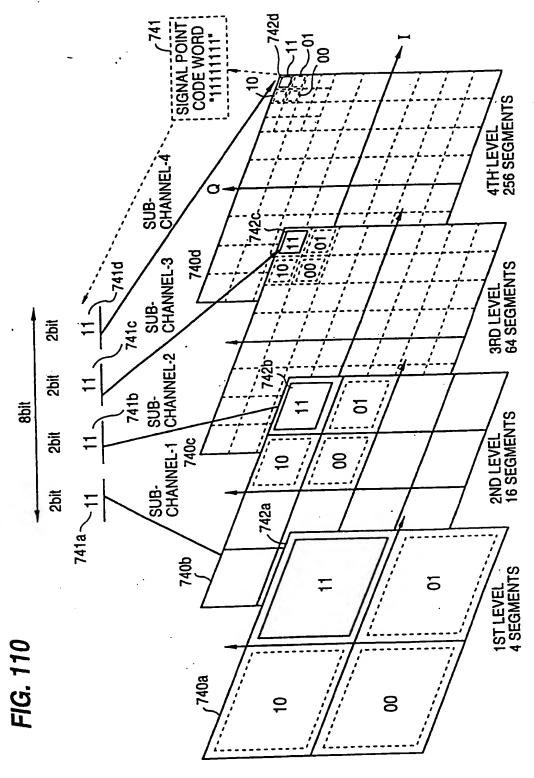


FIG. 111

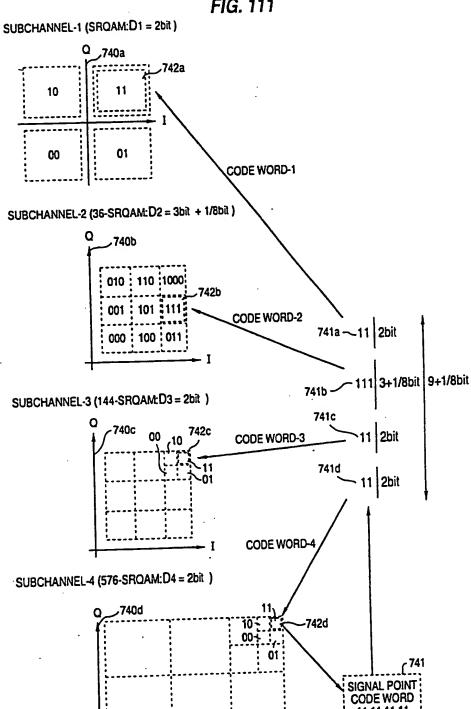
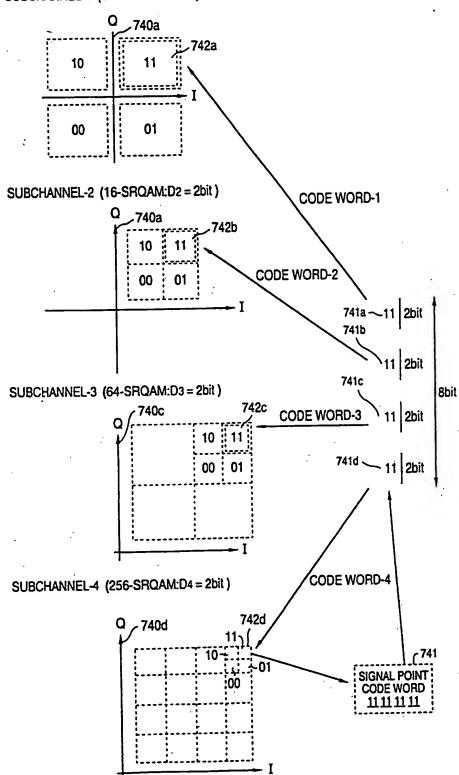
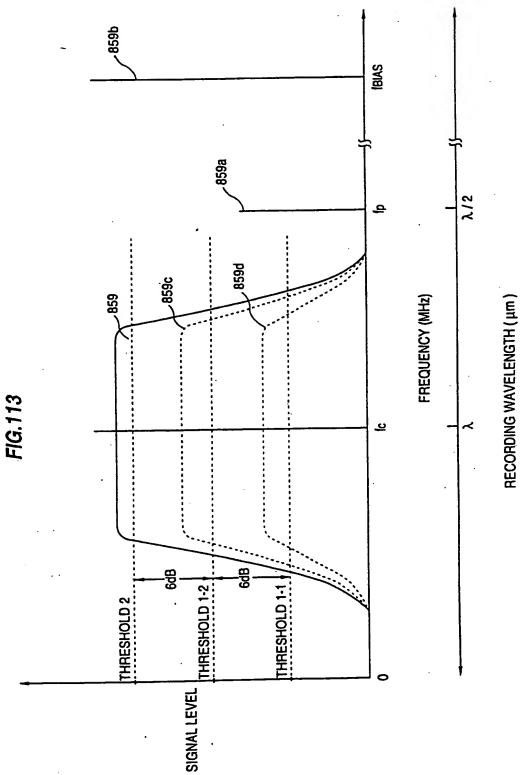
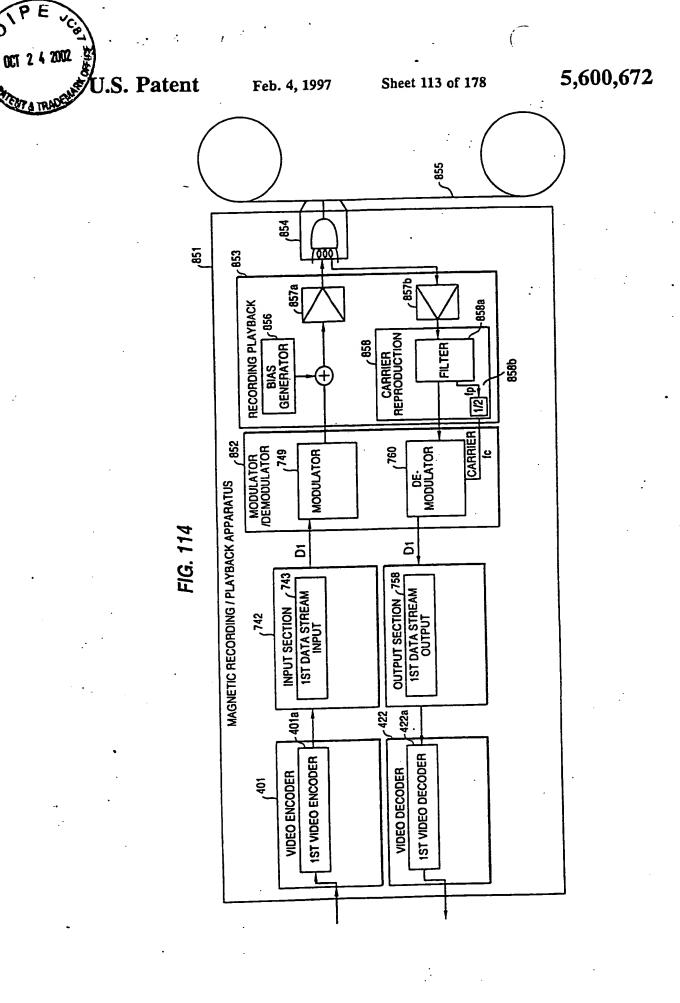


FIG. 112

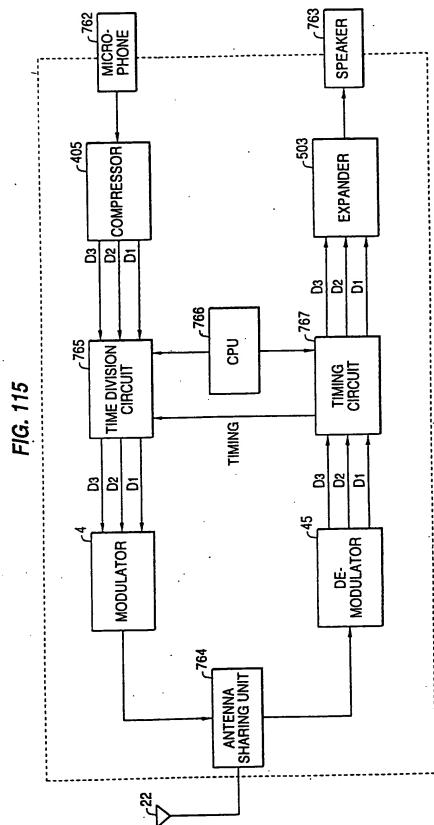
SUBCHANNEL-1 (SRQAM:D1 = 2bit)

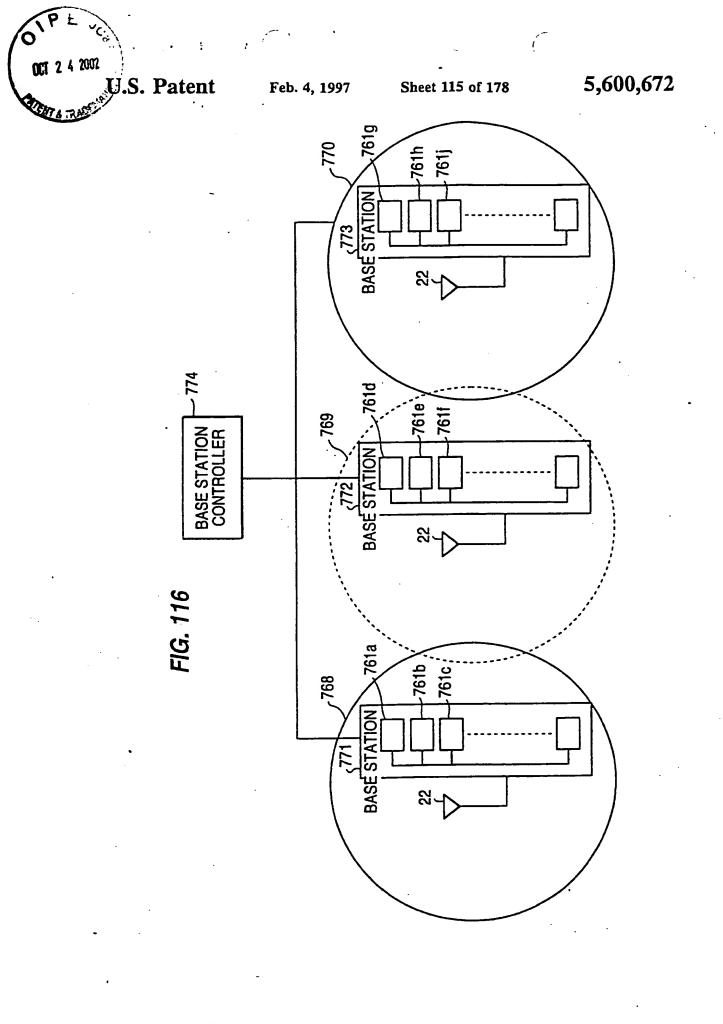


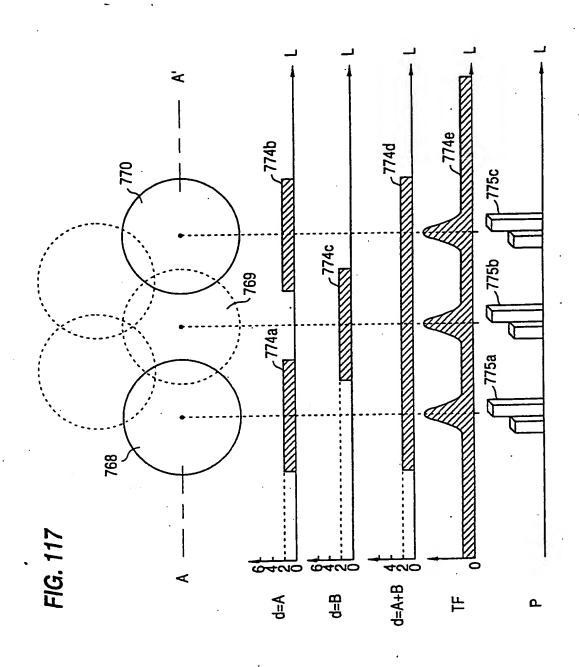




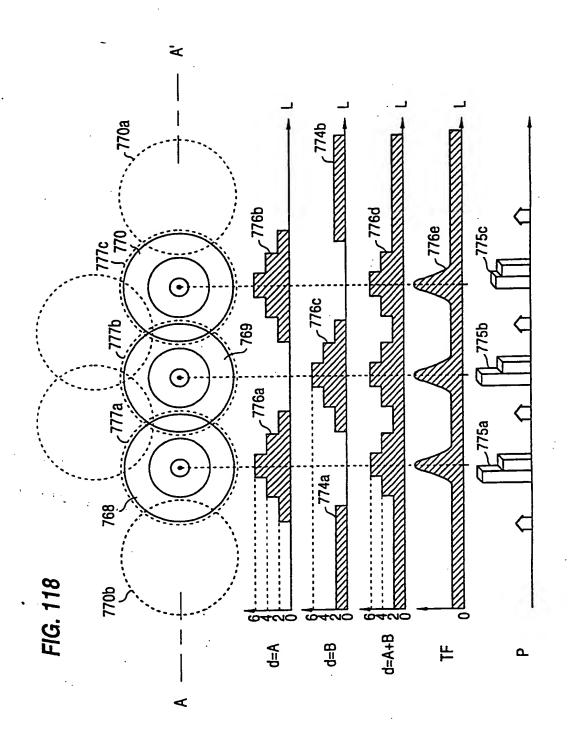


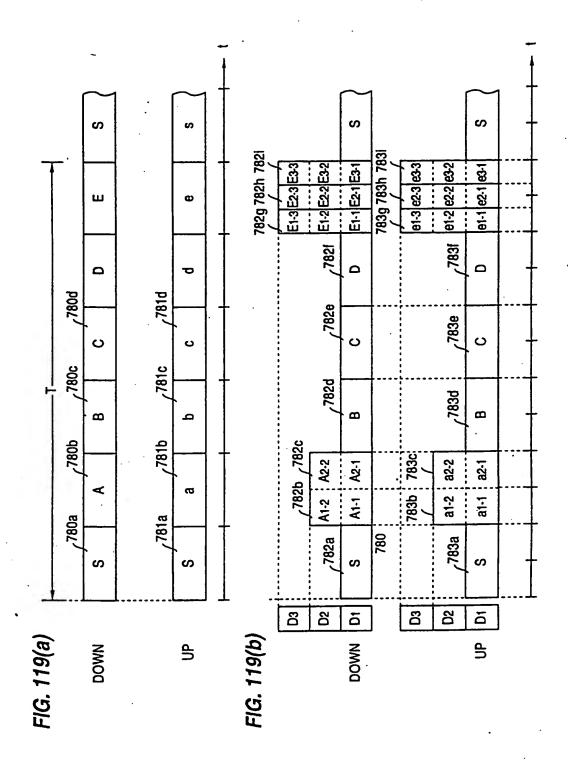




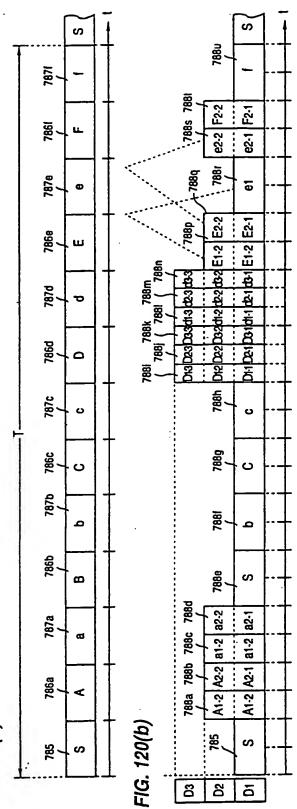


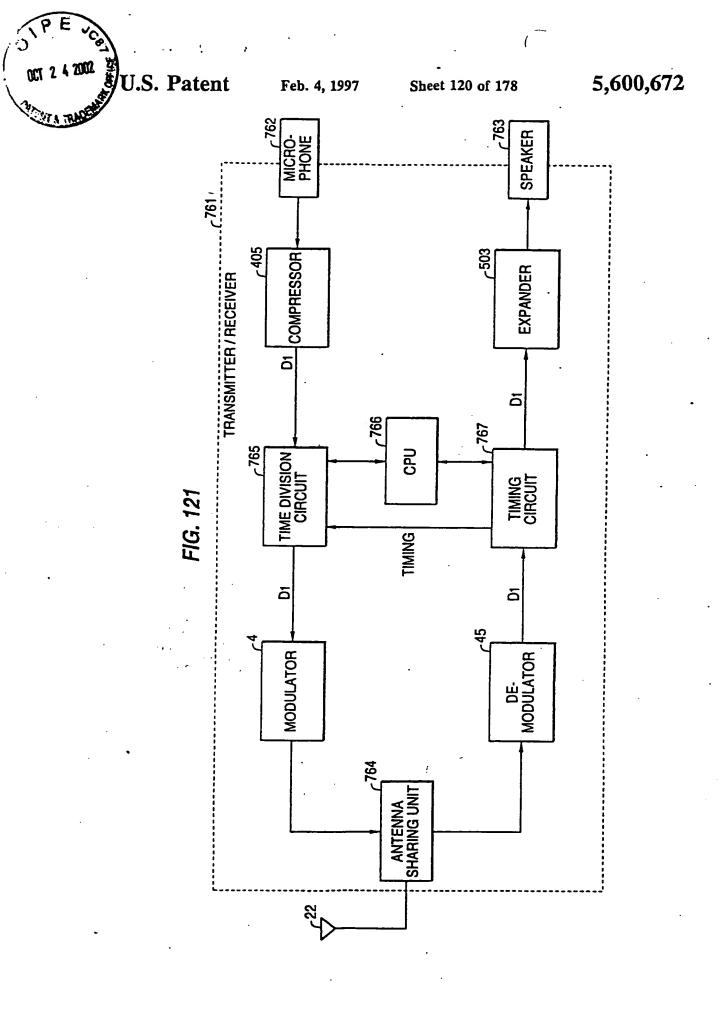


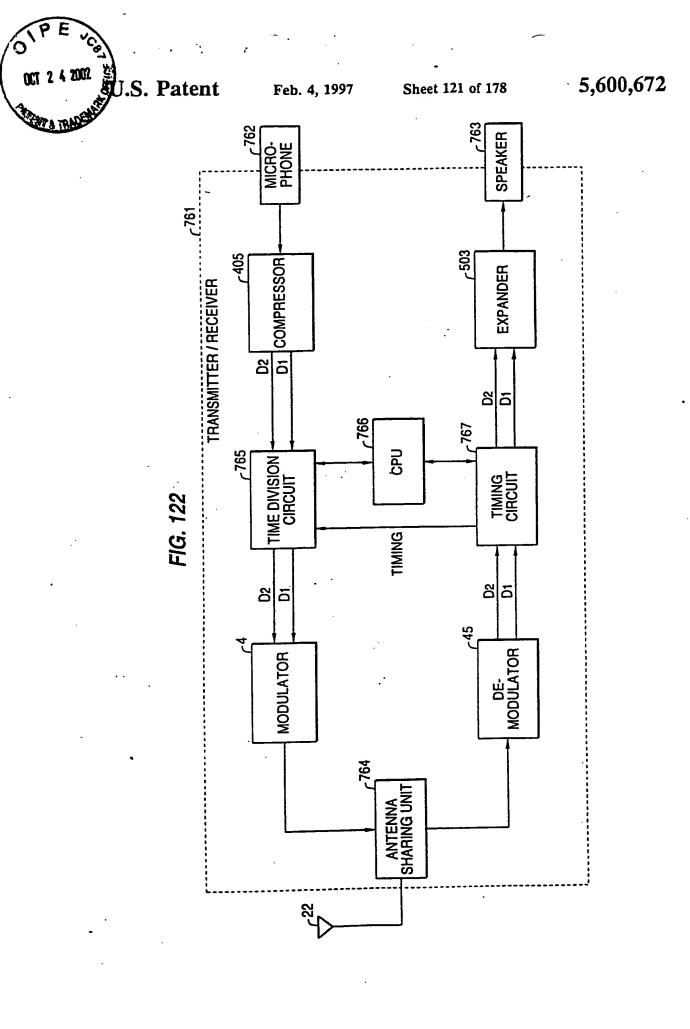


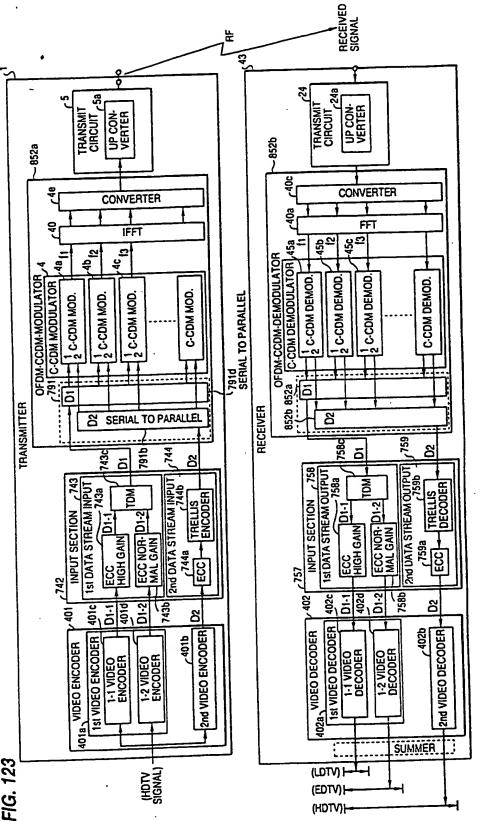


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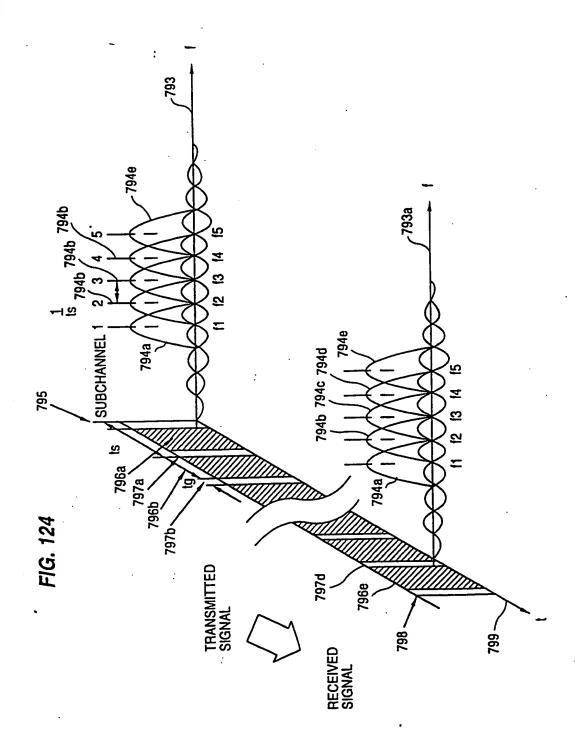


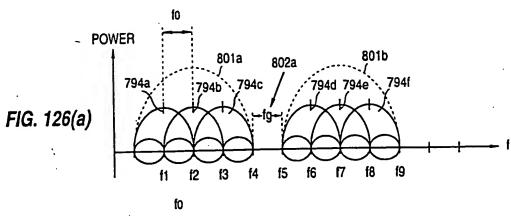


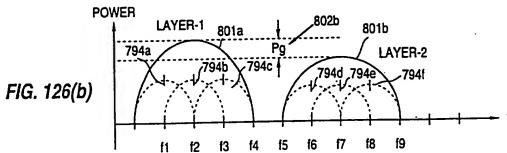


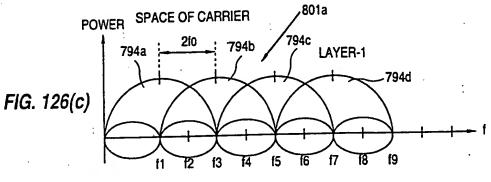


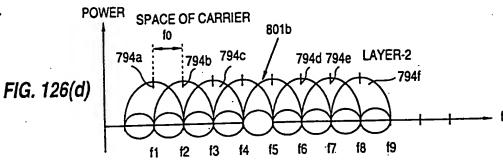














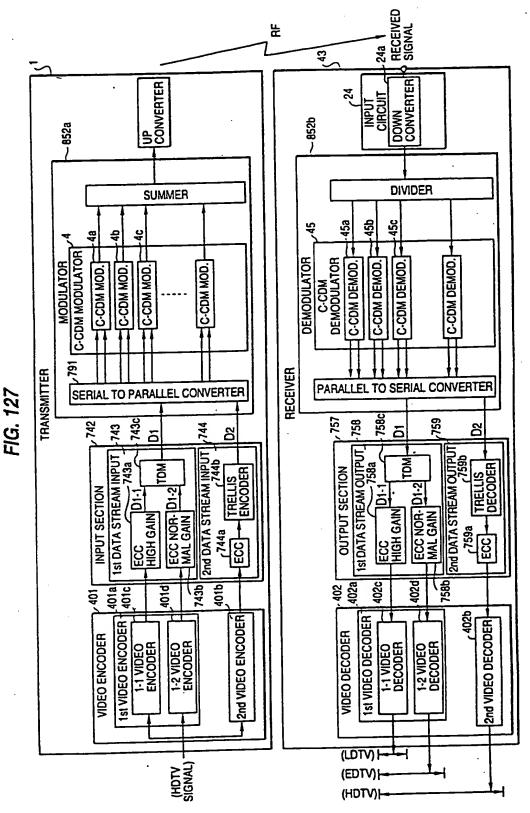


FIG. 128(a)

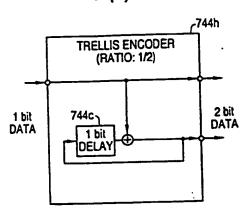


FIG. 128(d)

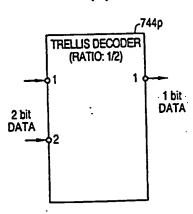


FIG. 128(b)

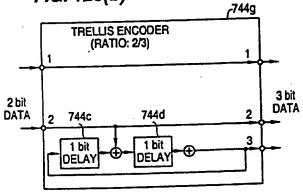


FIG. 128(e)

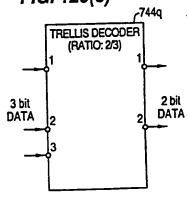


FIG. 128(c)

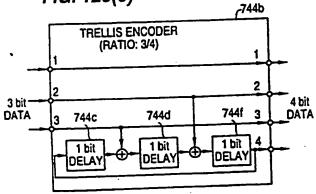
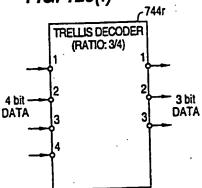
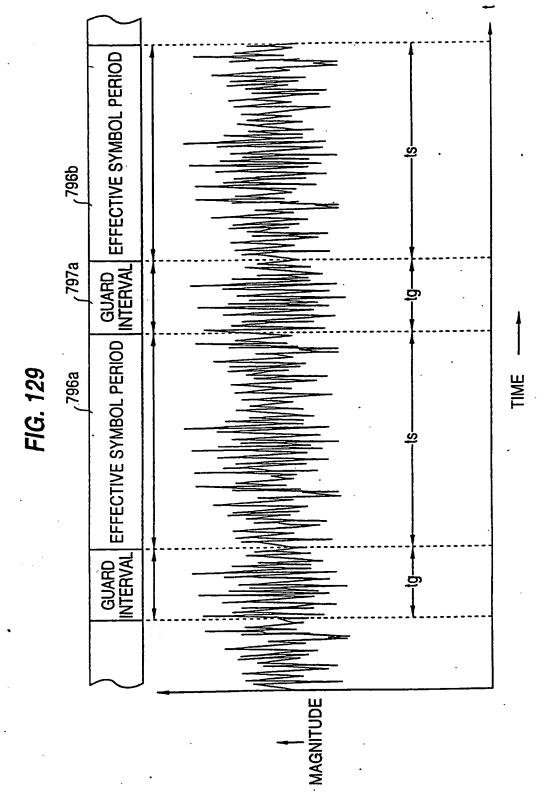


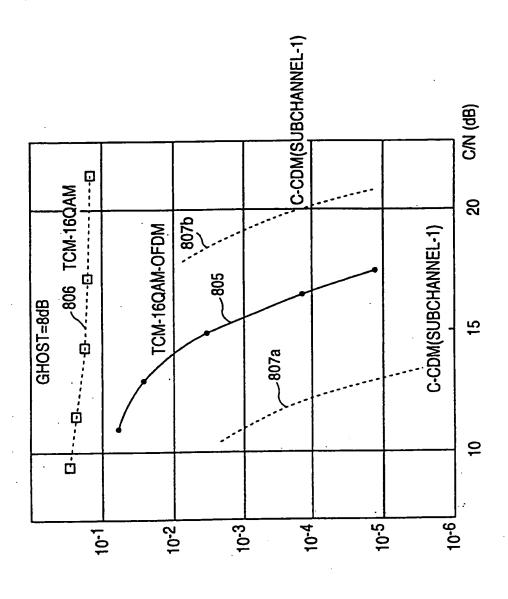
FIG. 128(f)

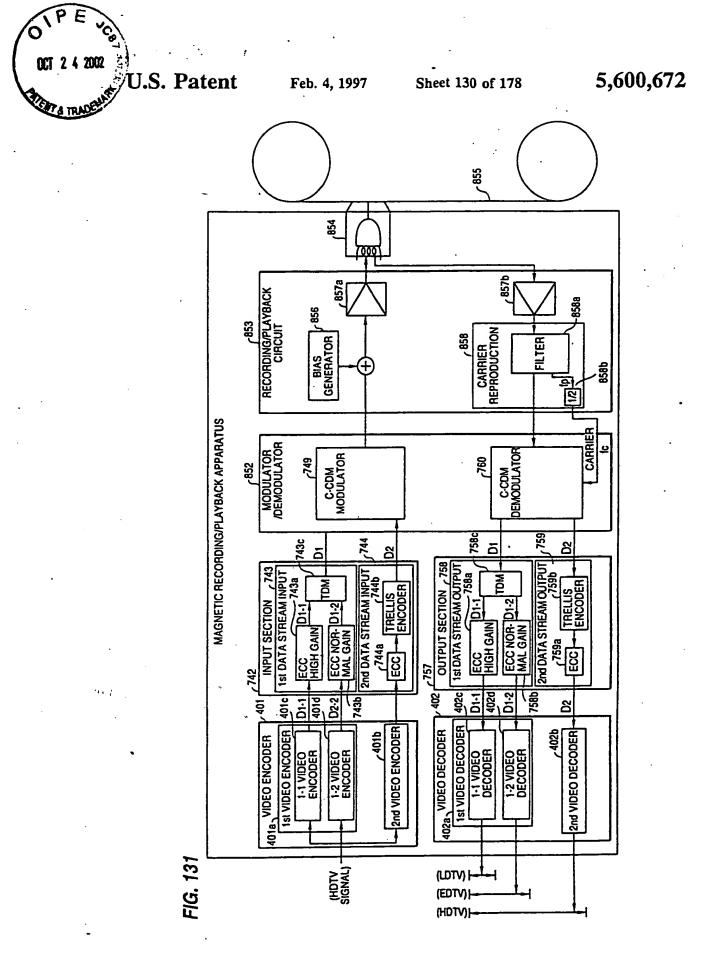


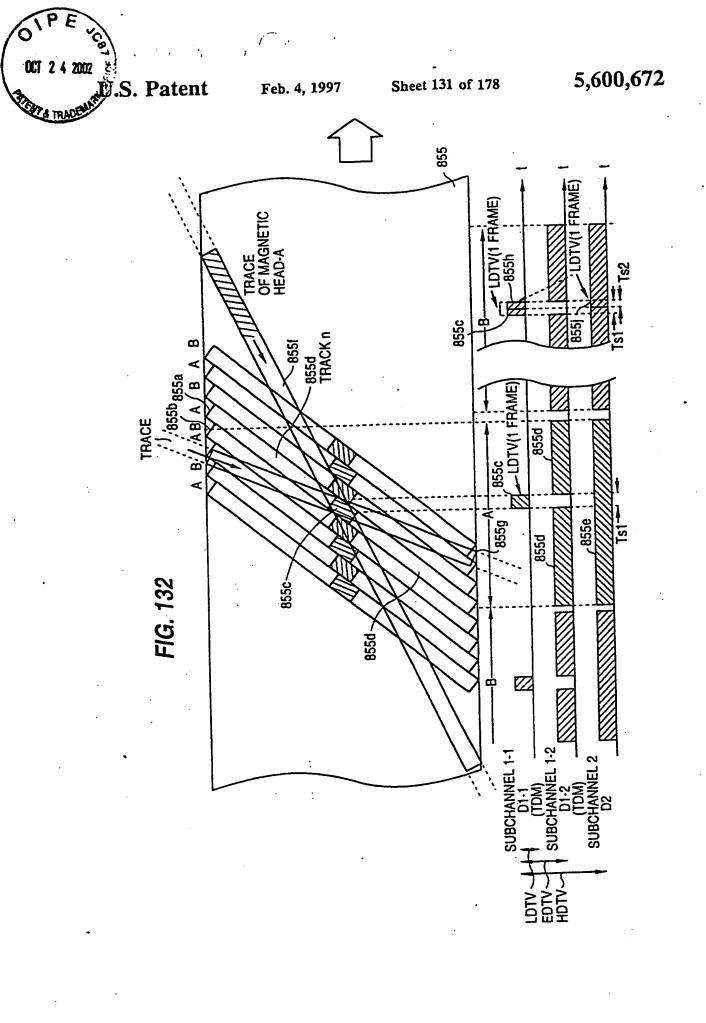


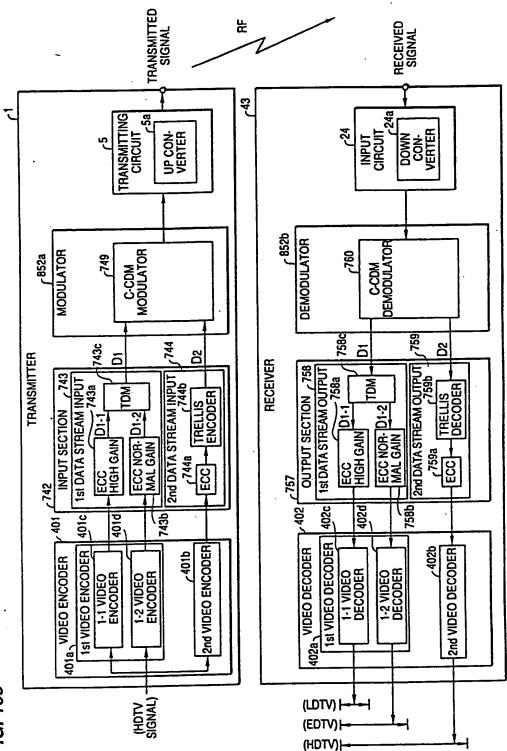
5,600,672



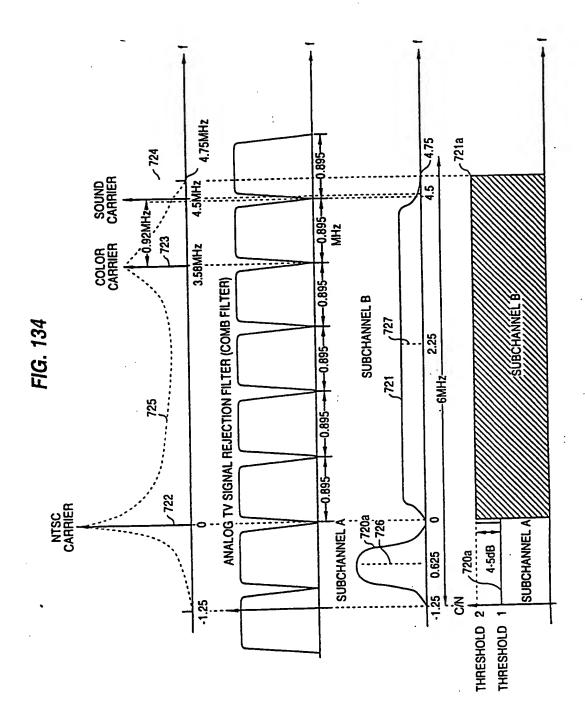


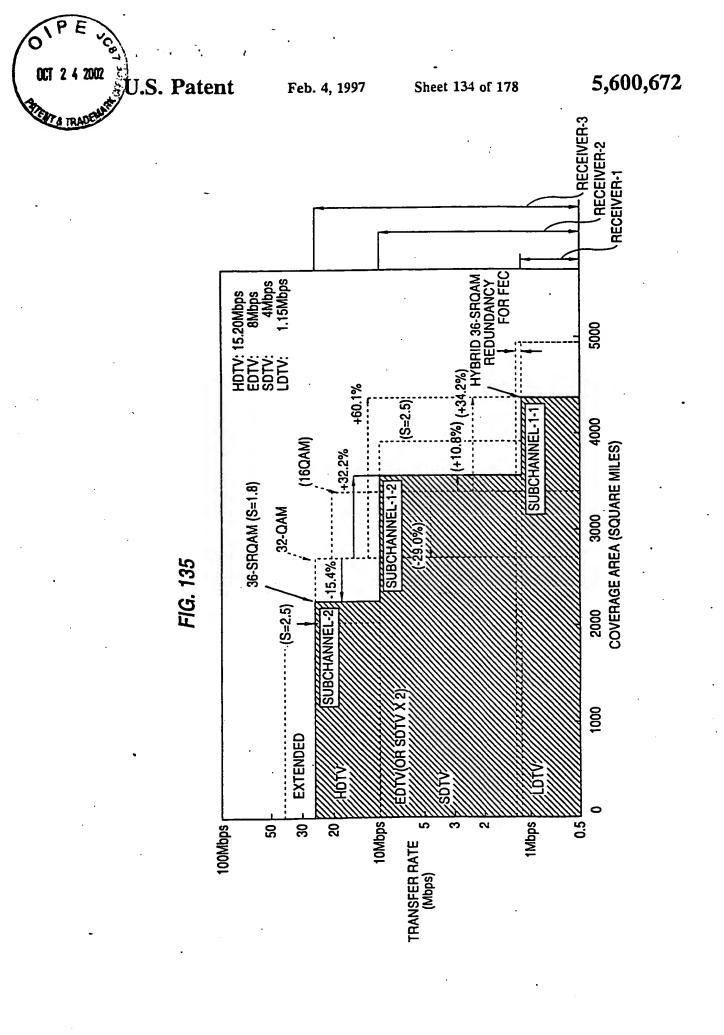


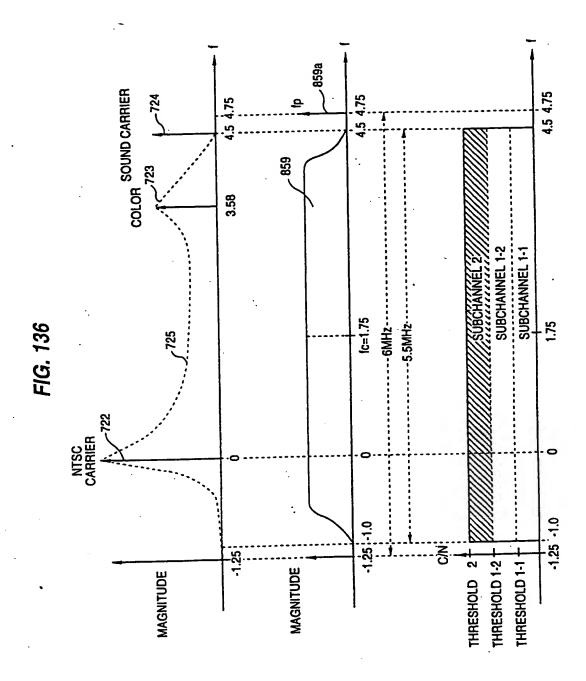




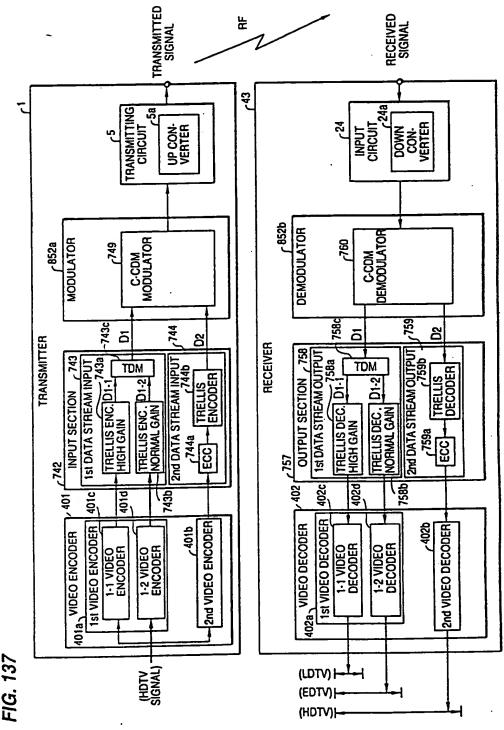












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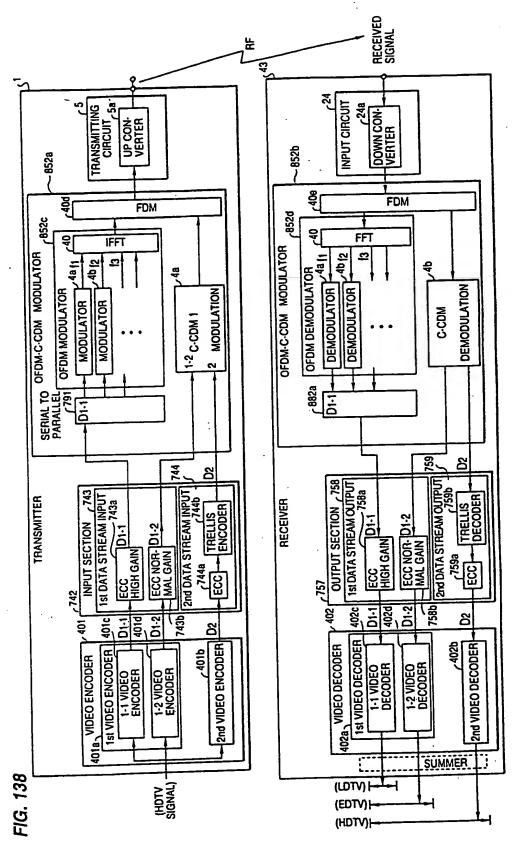


FIG. 139

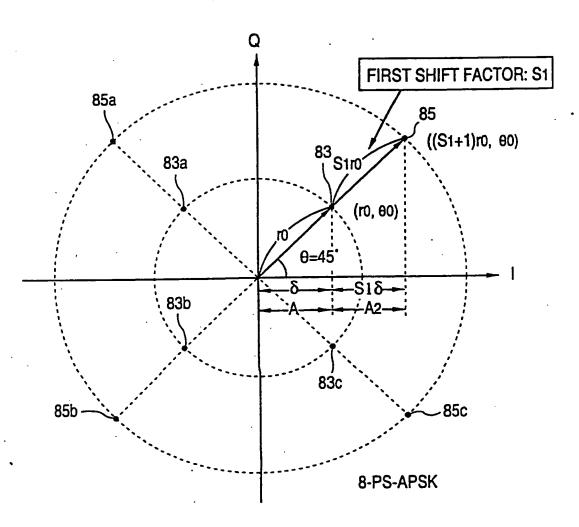
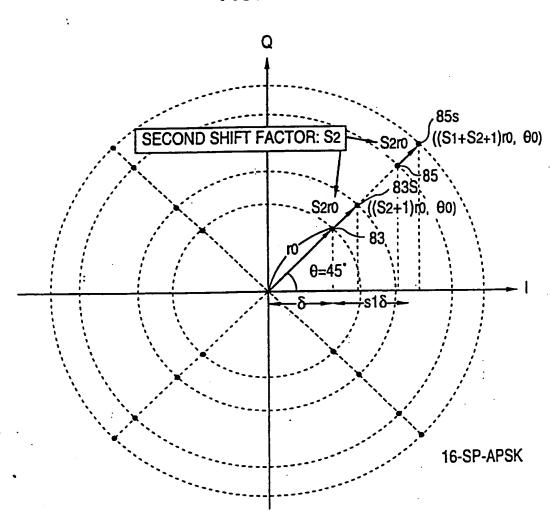


FIG. 140



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FIG. 141

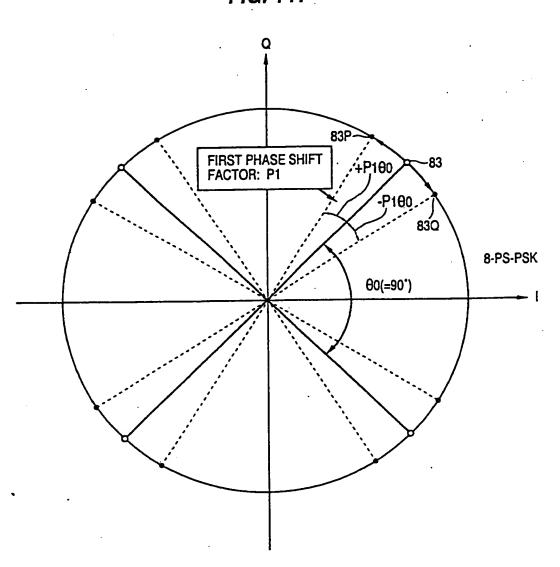
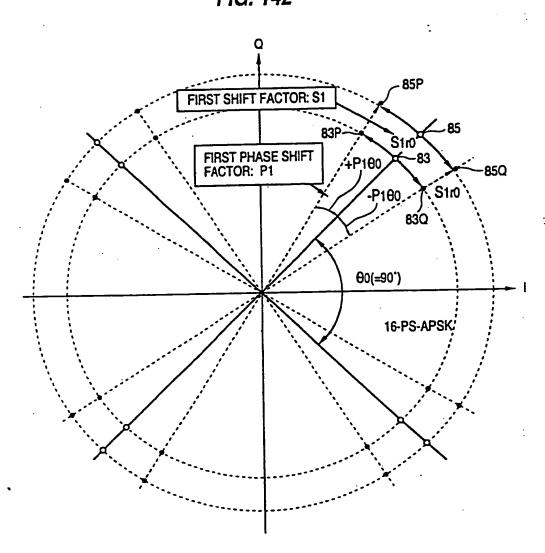
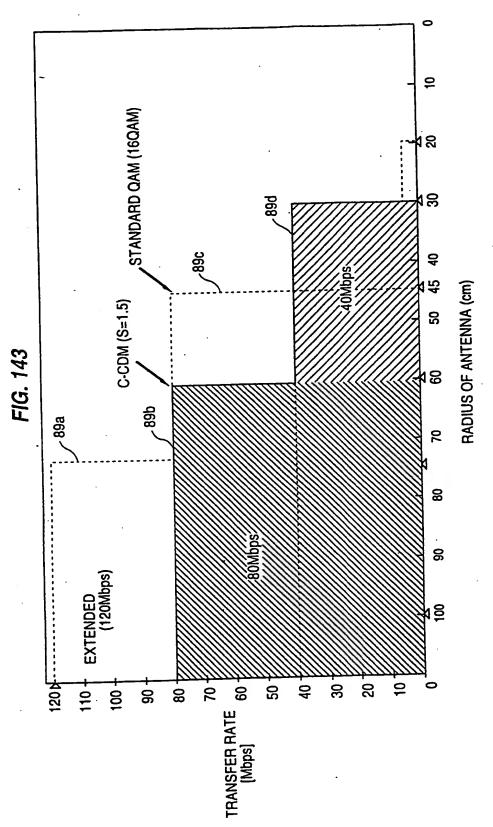


FIG. 142





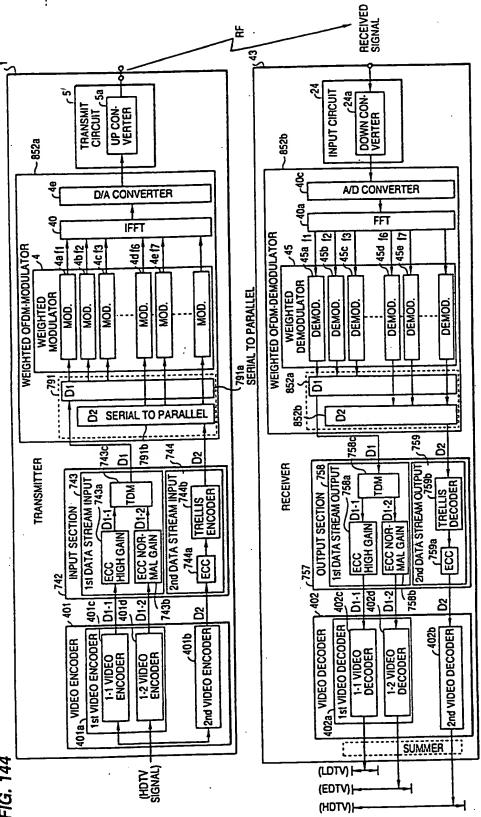


FIG. 145(a)

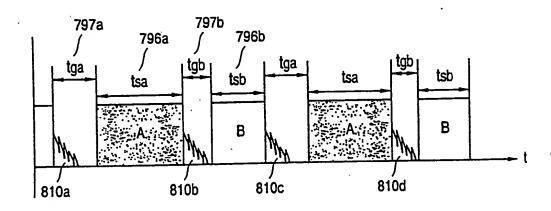
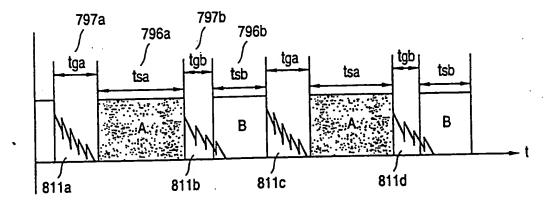
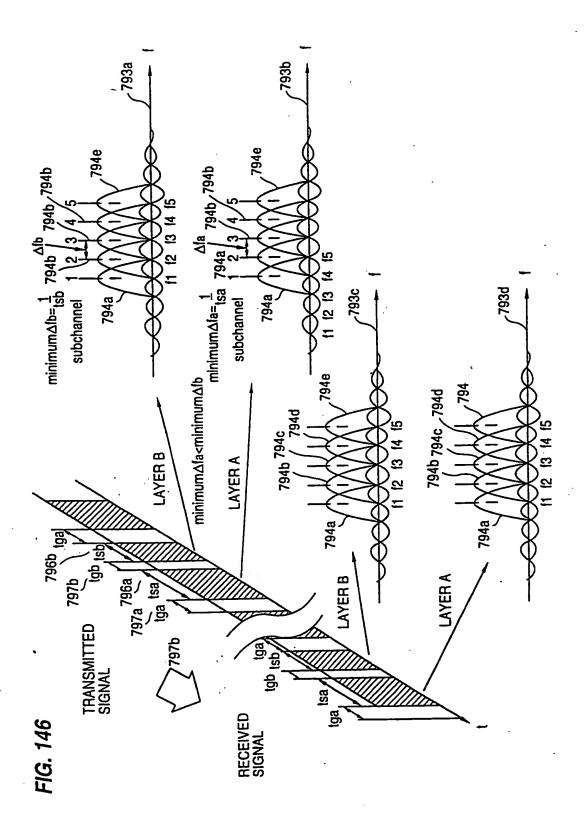
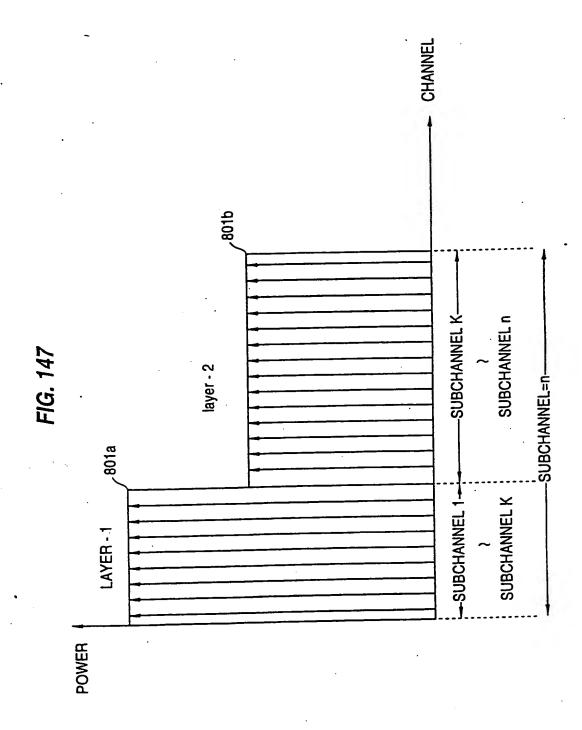


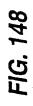
FIG. 145(b)

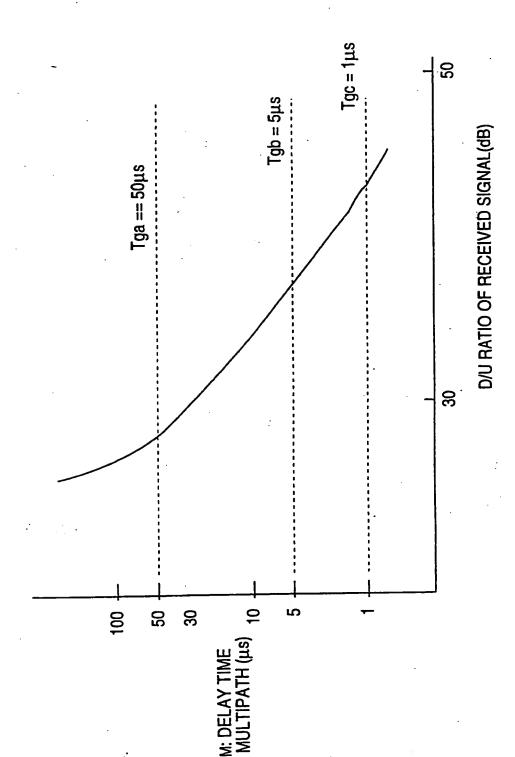


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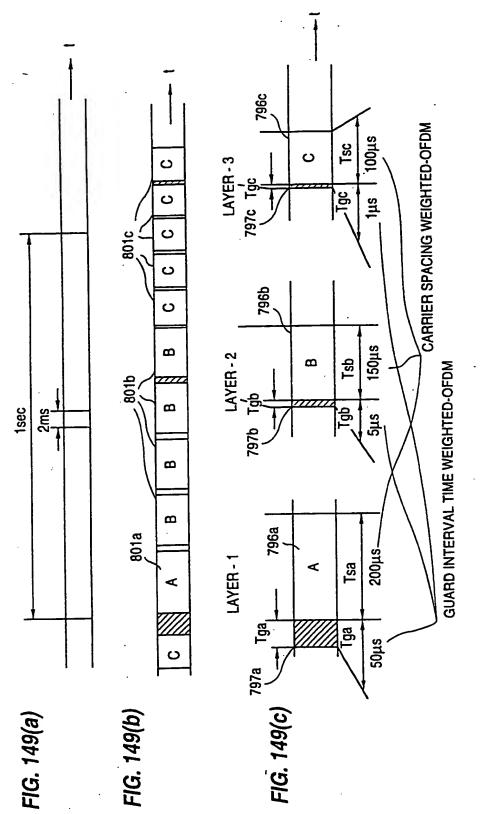


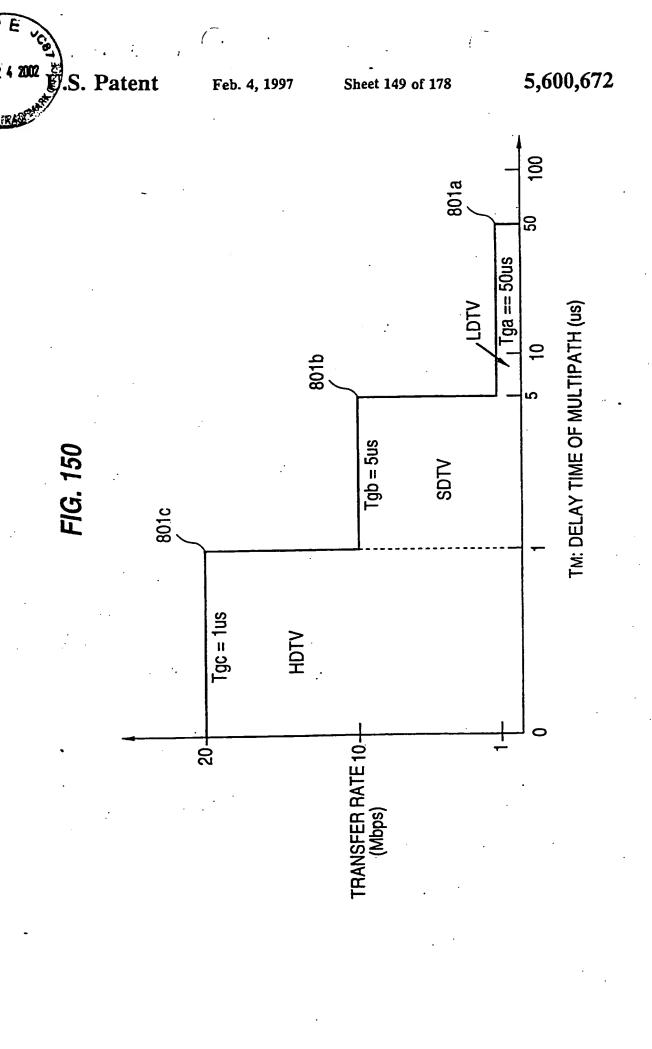


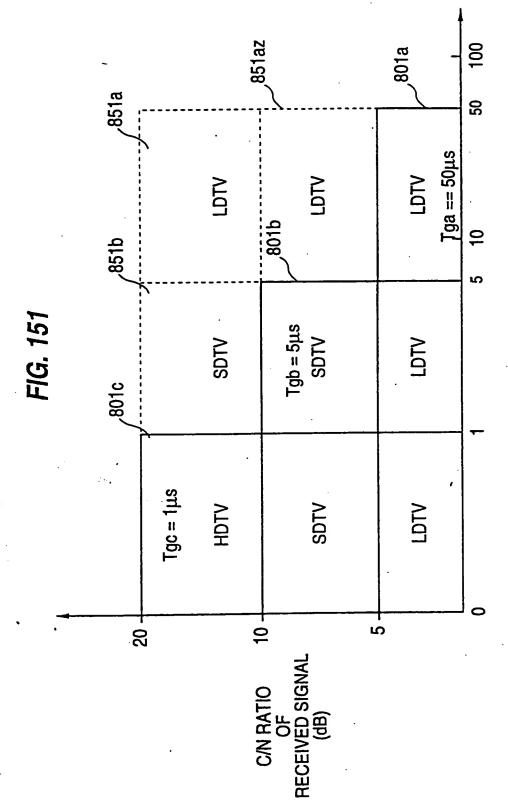


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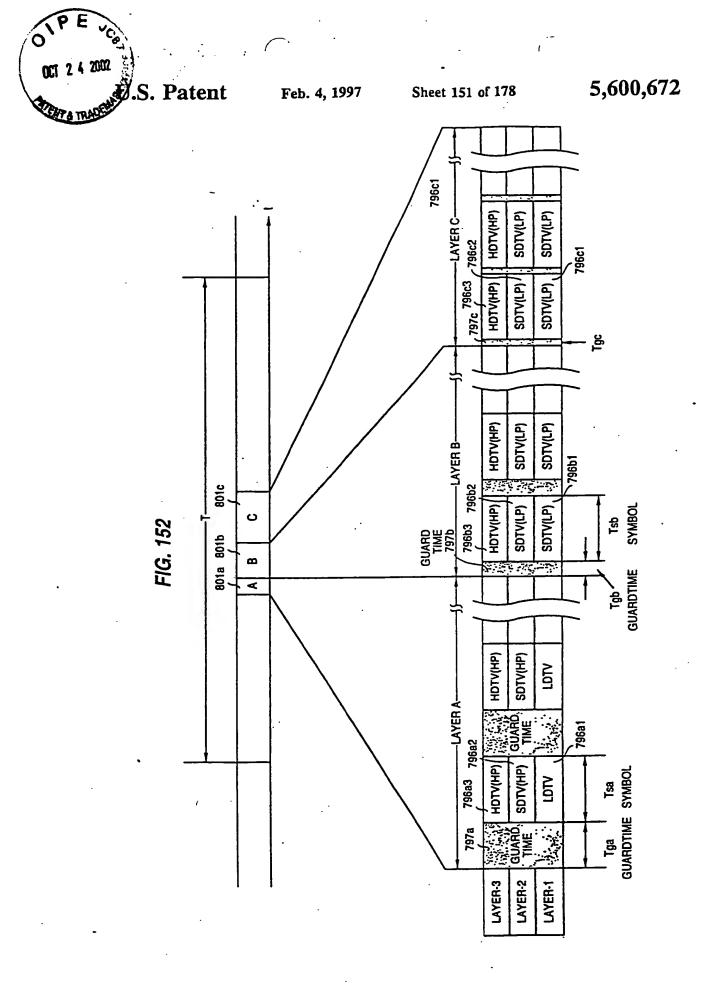


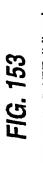


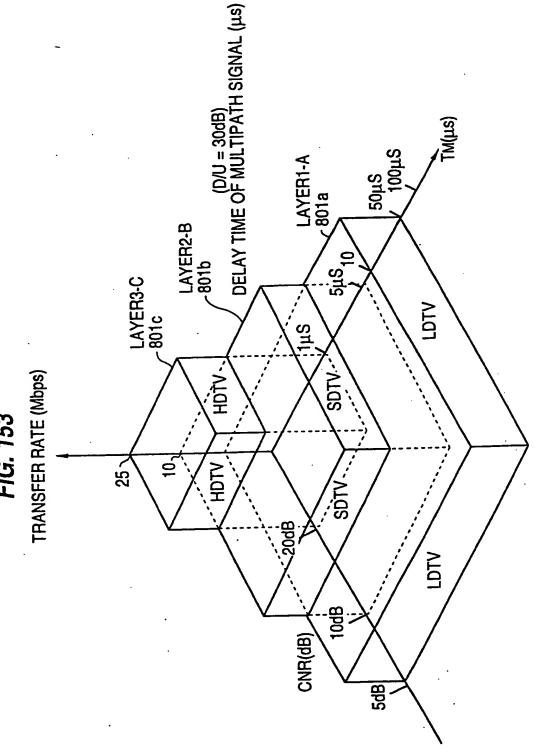


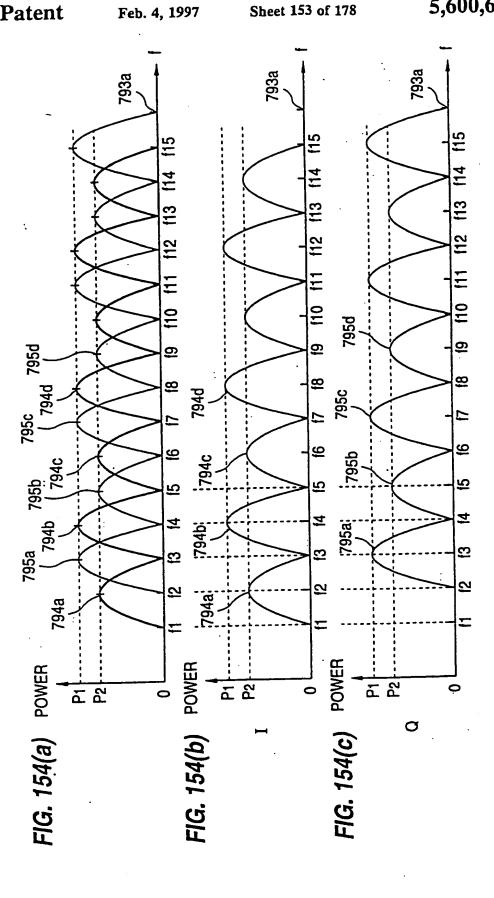


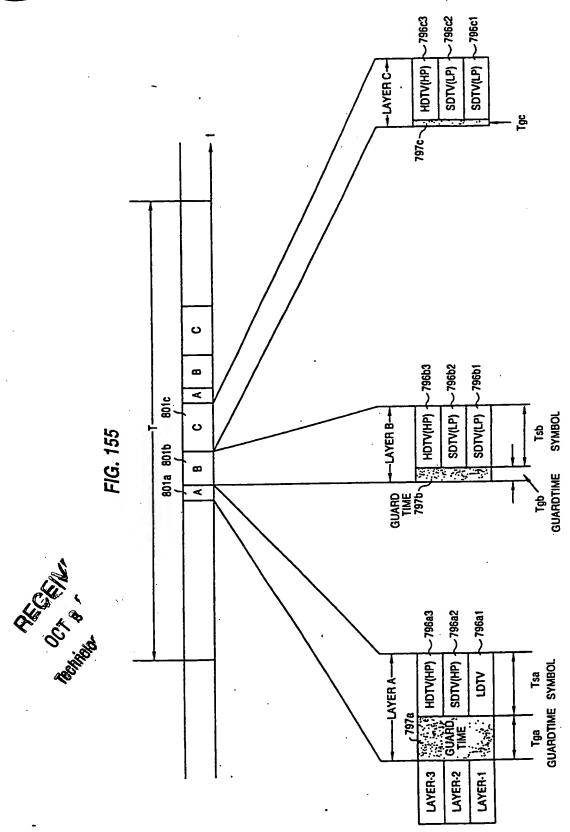
TM: DELAY TIME OF MULTIPATH(µs)

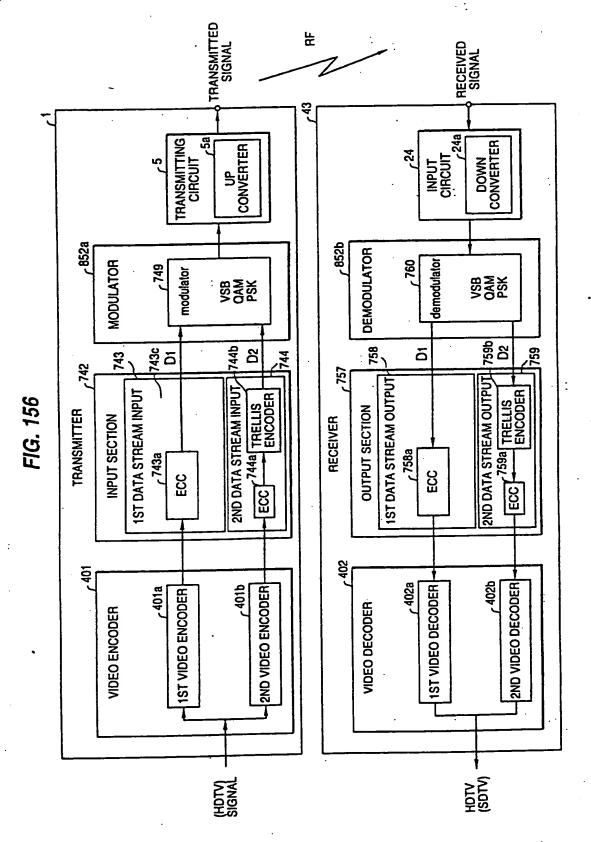


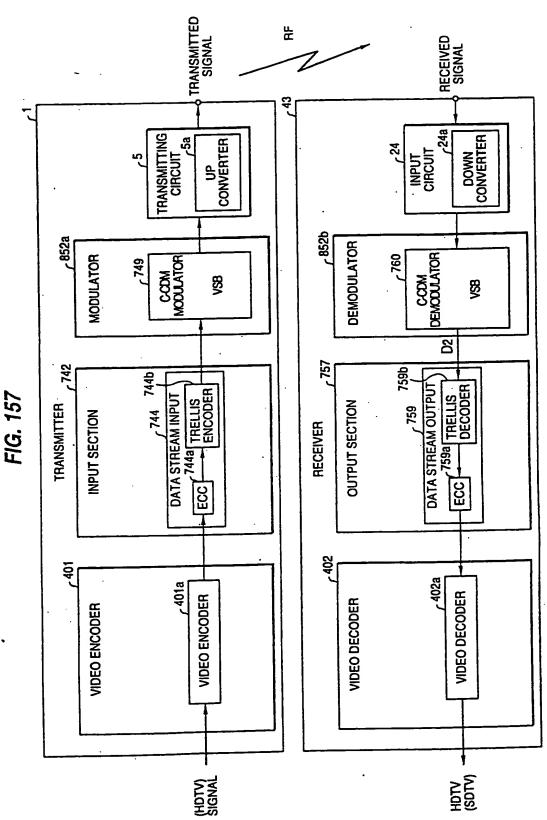












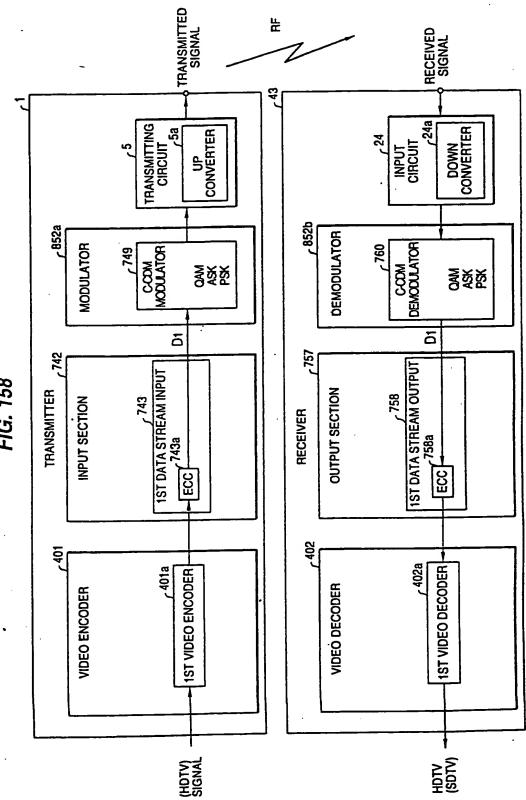
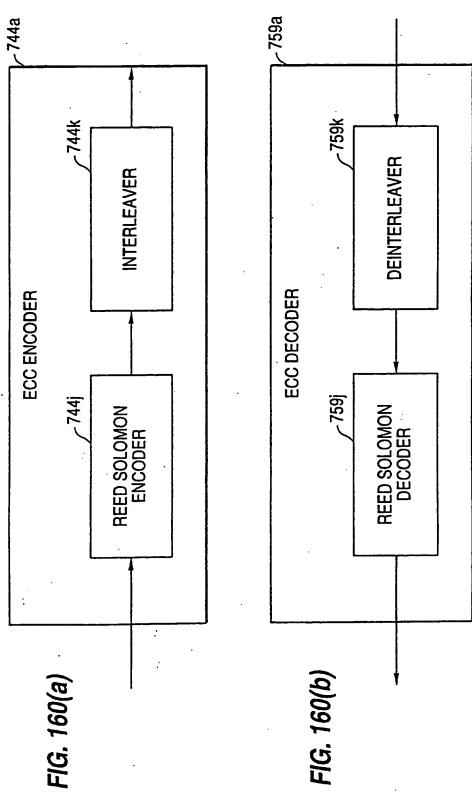
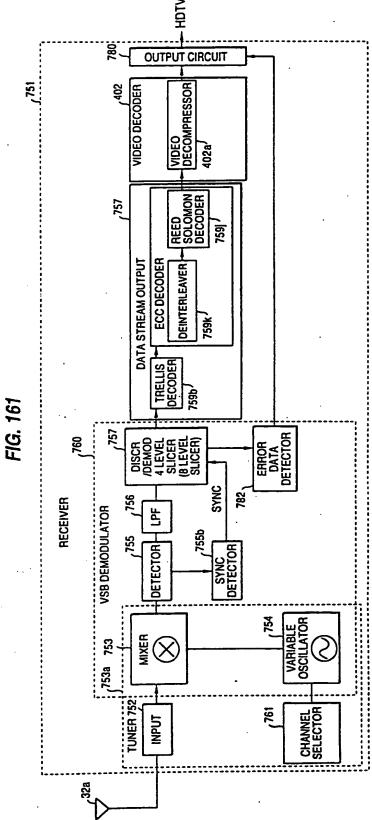
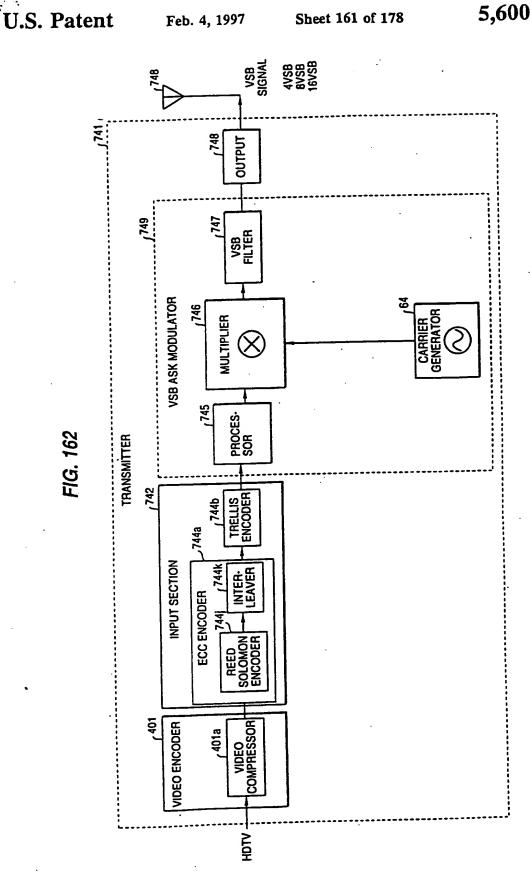


FIG. 158









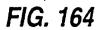
C/N(dB)

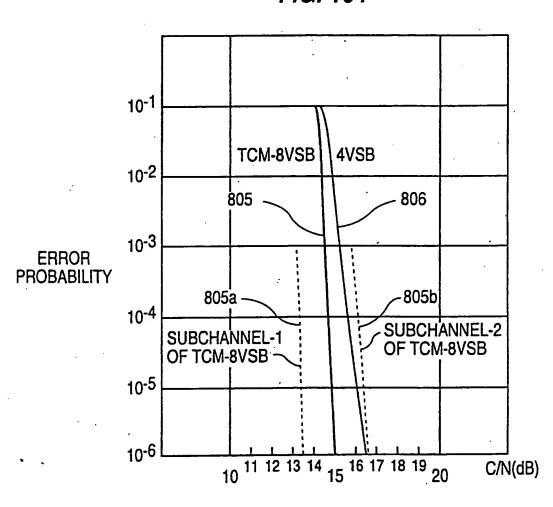
Patent Feb. 4, 1997

10 11 12 13 14 15 16 17 18 19 20

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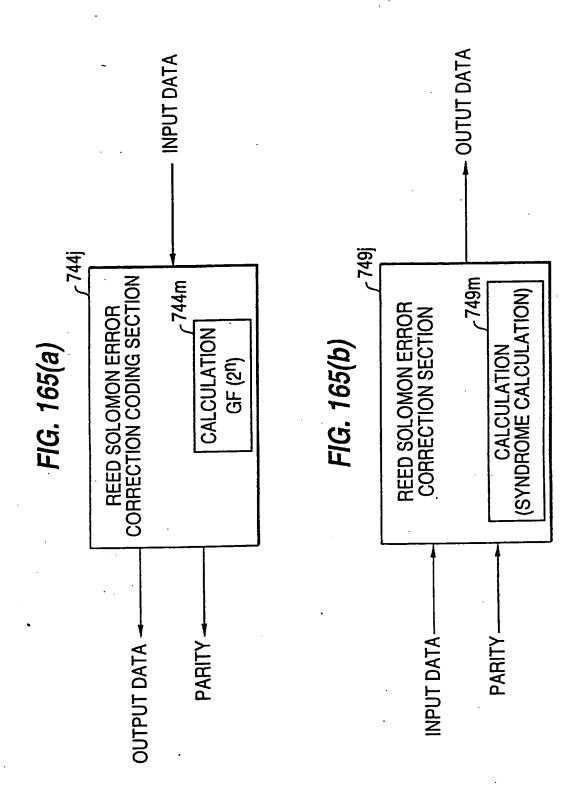
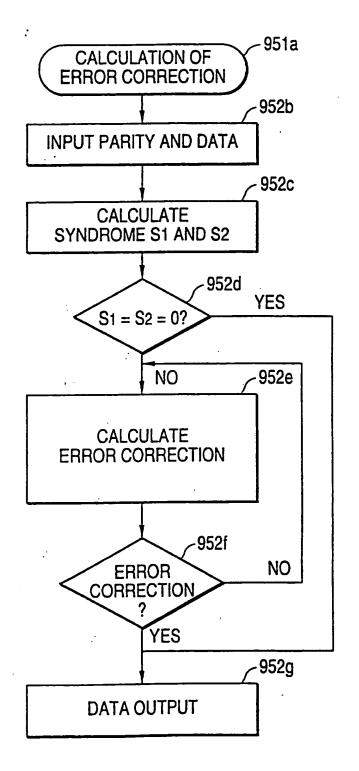
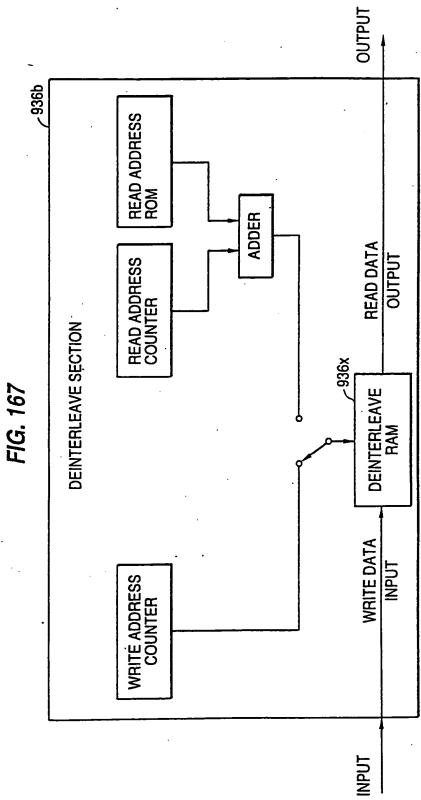


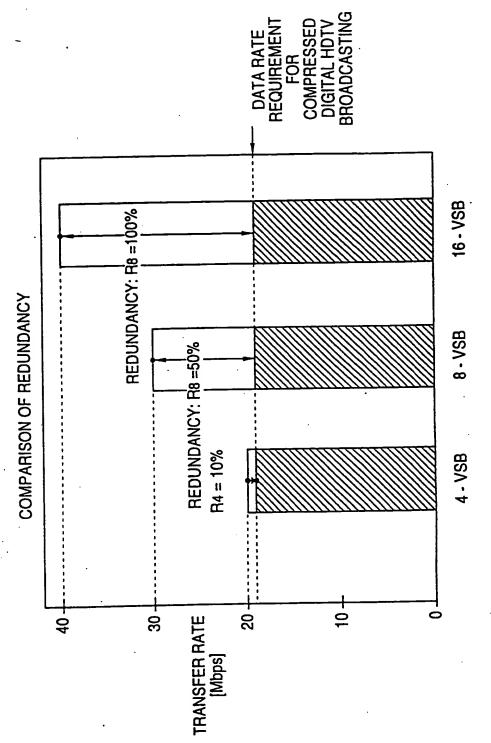


FIG. 166









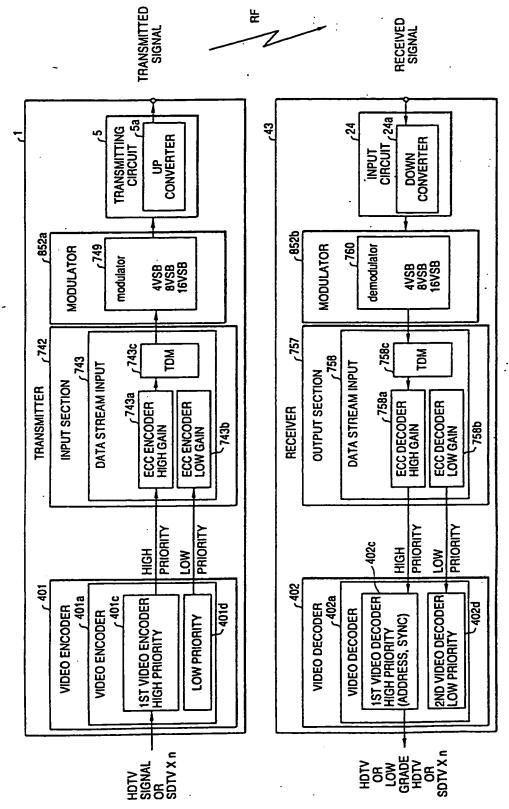


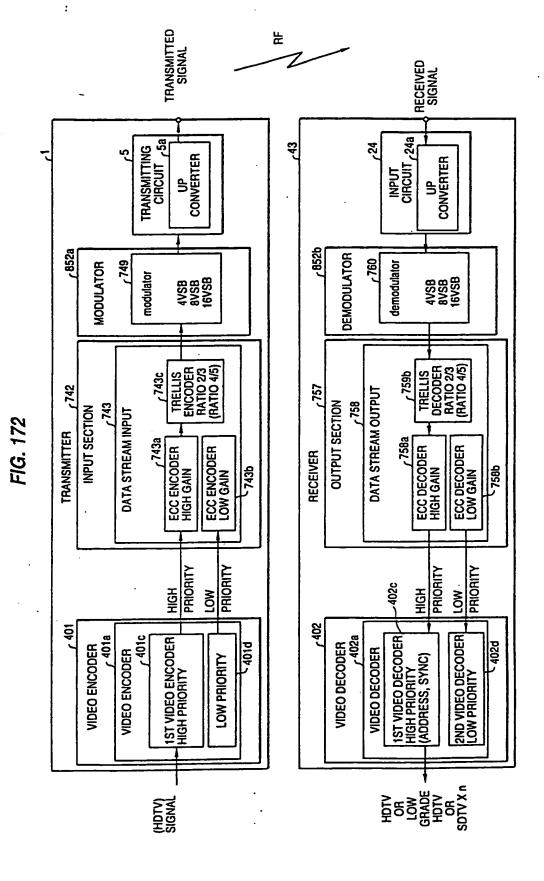
FIG. 170

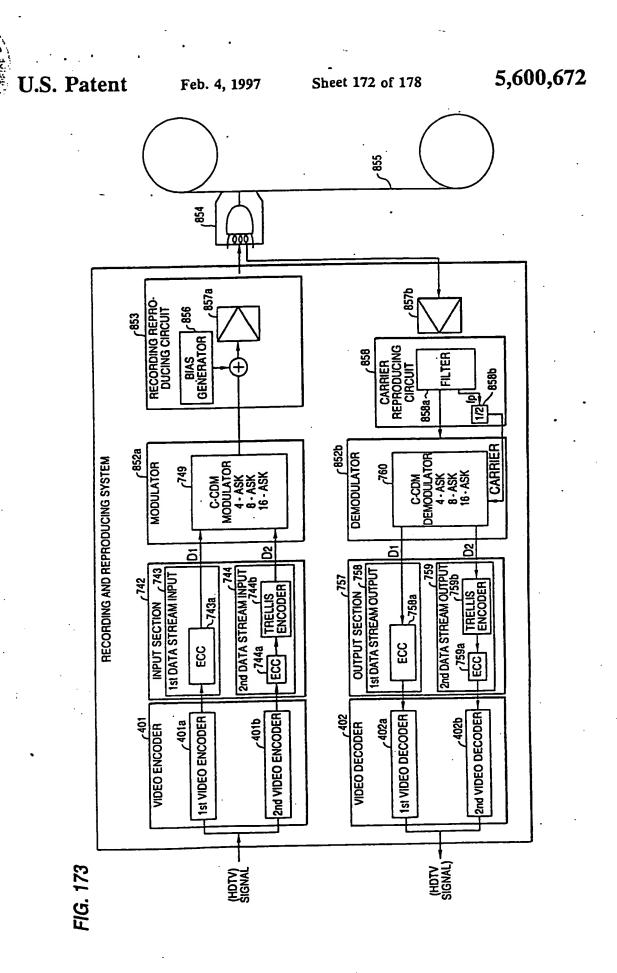
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OCI 2 4 2002 EU.S. Patent

TRANSMITTING SIGNAL RECEIVED SIGNAL INPUT CIRCUIT, 24a TRANSMITTING CIRCUIT , 5a UP CONVERTER DOWN CONVERTER r 852a DEMODULATOR 760 749 demodulator MODULATOR 8VSB (32QAM) 8VSB (32QAM) modulator 20 TRANSMITTER RECEIVER OUTPUT SECTION 7759 INPUT SECTION 744 DATA STREAM INPUT, 744b TRELLIS ENCODER RATIO 2/3 (RATIO 4/5) DATA STREAM OUTPUT, 759b TRELLIS DECODER RATIO 2/3 (RATIO 4/5) 757 744a 759a ECC ENCODER ECC DECODER 7 401a 7 402a £ 405 VIDEO DECODER **VIDEO ENCODER VIDEO ENCODER** VIDEO DECODER VTOH (VTOS) HDTV SIGNAL

FIG. 171



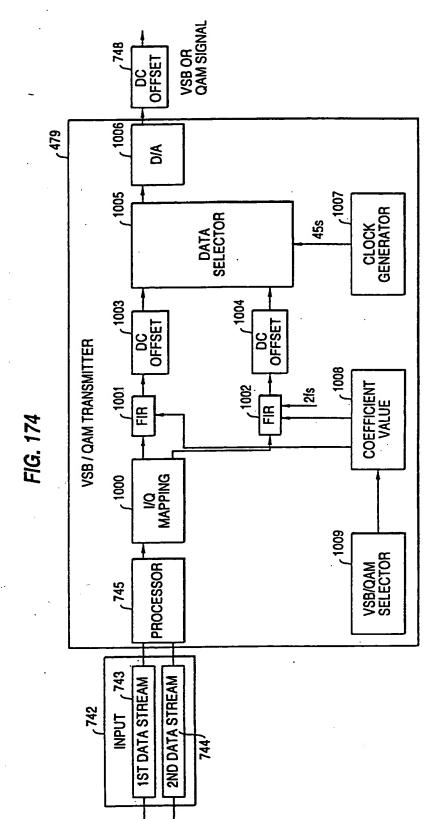


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Feb. 4, 1997

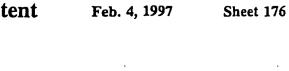
OCT 2 4 2002

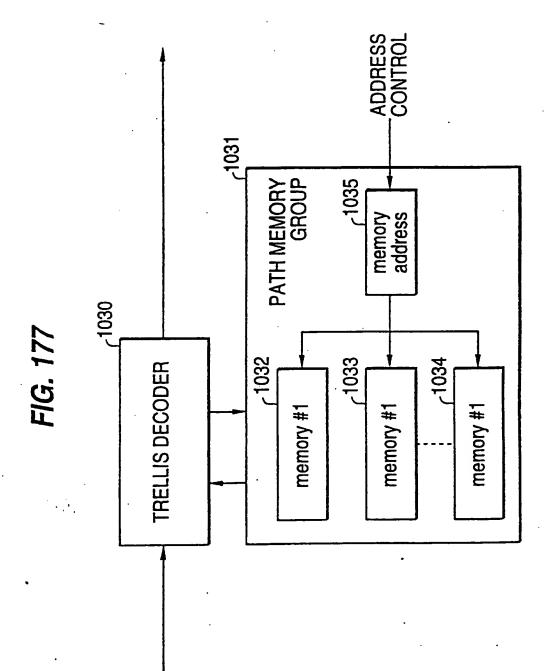


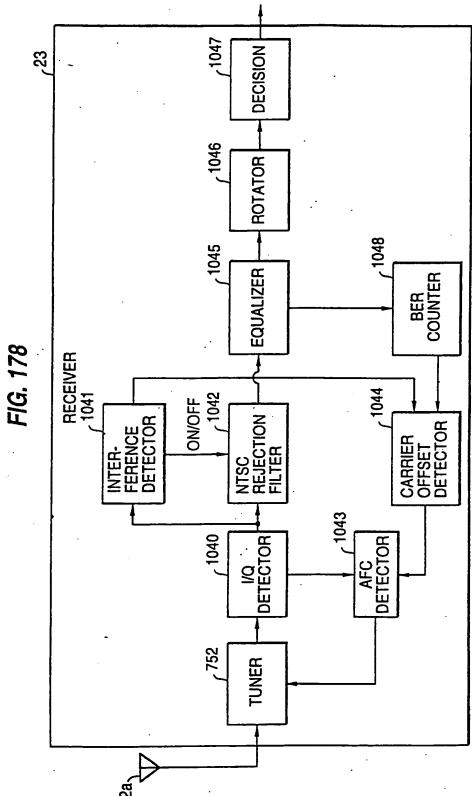
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